

PERIYAR PALKALAI NAGAR SALEM – 636011

DEGREE OF BACHELOR OF SCIENCE CHOICE BASED CREDIT SYSTEM

Syllabus for

B.SC. ELECTRONICS AND COMMUNICATION

(SEMESTER PATTERN) (For Candidates admitted in the Colleges affiliated to Periyar University from 2023 – 2024 onwards)

Programme Educational Objectives (PEOs):

Programme Educational Objectives (PEOs) are established through a consultation process. PEOs are broad statements that describe the career and professional accomplishments that the graduates can find opportunities in Sales, Marketing, Service and R&D Sectors.

The B.Sc. Degree programme graduates will

• **PEO 1** Practice the ethics of their profession consistent with a sense of social responsibility and develop their engineering design, problem -solving skills and aptitude for innovations as they work individually and in multi-disciplinary teams.

• **PEO 2** Communicate effectively and manage resources skill fully as members and leaders of the profession.

• **PEO 3** Be receptive to new technologies and attain professional competence through lifelong learning such as advanced degrees, professional registration, publications and other professional activities.

Programme Specific Outcomes:

On completion of the BSc Degree in Electronics and Communication, graduates will be able to

• **PSO1** Apply the fundamental concepts of electronics and communication to design a variety of components and systems for applications including Communication, Networking, Embedded systems, PCB, Mobile, TV and etc.

• **PSO2** Select and apply cutting-edge hardware and software tools to solve complex Electronics and Communication problems.

Programme Out comes:

On completion of the B.Sc. degree the Electronics and Communication graduates will be able to

• **PO1** Utilize the basic knowledge in Mathematics, science in Electronics and Communication areas, Computer and etc.

• **PO2** Identify formulate and solve complex problems to achieve demonstrated conclusions using Mathematical principles.

• **PO3** Design system components that meet the requirement of public safety and offer solutions.

• **PO4** Apply research-based knowledge to design and conduct experiments, analyze, synthesize and interpret the data pertaining to Electronics and arrive at valid conclusions.

• **PO5** Construct, choose and apply the techniques, resources and modern tools required for Electronics applications.

• **PO6** Examine the impact of Electronics solutions in global and environmental contexts and utilize the knowledge for sustained development.

• **PO7** Develop consciousness of professional, ethical and social responsibilities as experts in the field of Electronics.

Value additions in the Revamped Curriculum:

Semester	Newly introduced Components	Outcome / Benefits
Ι	Foundation Course To ease the transition of learning from higher secondary to higher education, providing an overview of the pedagogy of learning abstract Mathematics and simulating mathematical concepts to real world.	 Instil confidence among students Create interest for the subject
I, II, III, IV	Skill Enhancement papers (Discipline centric / Generic / Entrepreneurial) - -	 Industry ready graduates Skilled human resource Students are equipped with essential skills to make them employable Training on Computing / Computational skills enable the students gain knowledge and exposure on latest computational aspects Data analytical skills will enable students gain internships, apprenticeships, field work involving data collection, compilation, analysis etc. Entrepreneurial skill training will provide an opportunity for independent livelihood Generates self – employment Create small scale entrepreneurs Training to girls leads to women empowerment Discipline centric skill will improve the Technical knowhow of solving real life problems using ICT tools
III, IV, V & VI	Elective papers- An open choice of topics categorized under Generic and Discipline Centric	 tools Strengthening the domain knowledge Introducing the stakeholders to the State-of Art techniques from the streams of multi-disciplinary, cross disciplinary and inter disciplinary nature Students are exposed to Latest topics on Computer Science / IT, that require strong mathematical background Emerging topics in higher education / industry / communication network / health sector etc. are introduced with hands-on-training, facilitates designing of mathematical models in the respective sectors

PERIYA	AR UNIVER	SITY		
IV	Industrial S	tatistics	•	Exposure to industry moulds students into solution providers
			•	Generates Industry ready graduates
			•	Employment opportunities enhanced
II year Vacation activity	Internship Industrial T			Practical training at the Industry/ Banking Sector / Private/ Public sector organizations / Educational institutions, enable the students gain professional experience and also become responsible citizens.
V	Project with	n Viva –	•	Self-learning is enhanced
Semeste r			•	Application of the concept to real situation is conceived resulting in tangible outcome
VI Semeste r	Semeste Professional		•	Curriculum design accommodates all category of learners; _Mathematics for Advanced Explain' component will comprise of advanced topics in Mathematics and allied fields, for those in the peer group / aspiring researchers;
			•	_Training for Competitive Examinations' – caters to the needs of the aspirants towards most sought - after services of the nation viz, UPSC, CDS, NDA, Banking Services, CAT, TNPSC group services, etc.
Extra Cree	dits:		•	To cater to the needs of peer learners /
For Advanced Learners / Honors degree			research aspirants	
1				Competency, Professional Communication

				1	1		1							I			T
Sem I	Cre dit	Н	Sem II	Cre dit	H	Sem III	Cre dit	Н	Sem IV	Cre dit	Н	Sem V	Cre dit	Н	Sem VI	Cre dit	Н
Part 1. Langua ge – Tamil	3	6	Part1. Langua ge – Tamil	3	6	Part1. Language – Tamil	3	6	Part1. Langua ge – Tamil	3	6	5.1 Core Cours e – \CC IX	4	5	6.1 Core Course - CC XIII	4	6
Part.2 English	3	6	Part2 English	3	6	Part2 English	3	6	Part2 English	3	6	5.2 Core Cours e – CC X	4	5	6.2 Core Course - CC XIV	4	6
1.3 Core Course – CC I	5	5	23 Core Course – CC III	5	5	3.3 Core Course – CC V	5	5	4.3 Core Course – CC VII Core Industry Module	5	5	5. 3.Cor e Cours e CC -XI	4	5	6.3 Core Course - CC XV	4	6
1.4 Core Course – CC II	5	5	2.4 Core Course – CC IV	5	5	3.4 Core Course – CC VI	5	5	4.4 Core Course - CC VIII	5	5	5. 4.Cor e Cours e -/ Proje ct with viva- voce CC - XII	4	5	6.4 Electiv e -VII Generi c/ Discipl ine Specifi c	3	5
1.5 Elective I Generic / Discipli ne Specific	3	4	2.5 Elective II Generic / Discipli ne Specific	3	4	3.5 Elective III Generic/ Disciplin e Specific	3	4	4.5 Elective IV Generic / Discipli ne Specific	3	3	5.5 Electi ve V Gener ic/ Disci pline Speci fic	3	4	6.5 Elective VIII Generi c/ Discipl ine Specifi c	3	5
1.6 Skill Enhanc ement Course SEC-1	2	2	2.6 Skill Enhanc ement Course SEC-2	2	2	3.6 Skill Enhance ment Course SEC-4, (Entrepre neurial Skill)	1	1	4.6 Skill Enhanc ement Course SEC-6	2	2	5.6 Electi ve VI Gener ic/ Disci pline Speci fic	3	4	6.6 Extensi on Activit y	1	-
1.7 Skill Enhanc ement - (Founda tion Course)	2	2	2.7 Skill Enhanc ement Course –SEC-3	2	2	3.7 Skill Enhance ment Course SEC-5	2	2	4.7 Skill Enhanc ement Course SEC-7	2	2	5.7 Value Educa tion	2	2	6.7 Profess ional Compe tency Skill	2	2

Credit Distribution for UG Programmes

PERIN	AF	UNIV	ERS	ITY	Y										
					3.8 E.V.S.	-	1	4.8 E.V.S	2	1	5.8 Sum mer Intern ship /Indus trial Traini ng	2			
23	6 3 0		23	3 0		22	3 0		25	3 0		26	3 0	21	3 0
	Total – 140 Credits														

Choice Based Credit System (CBCS), Learning Outcomes Based Curriculum Framework (LOCF) Guideline Based Credit and Hours Distribution System for all UG courses including Lab Hours

Part	List of Courses	Credit	No. of Hours
D 1			nouis
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses & Elective Courses [in Total]	10	14
	Skill Enhancement Course SEC-1	-	2
Part-4	Foundation Course	2	2
		18	30

First Year – Semester-I

Semester-II

Part	List of Courses	Credit	No. of
			Hours
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses & Elective Courses including laboratory [in Total]	14	14
Part-4	Skill Enhancement Course -SEC-2	2	2
	Skill Enhancement Course -SEC-3 (Discipline / Subject Specific)	2	2
		24	30

Second Year – Semester-III

Part	List of Courses	Credit	No. of Hours
Part-1	Language - Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses & Elective Courses including laboratory [in Total]	10	14
Part-4	Skill Enhancement Course -SEC-4 (Entrepreneurial Based)	-	1
	Skill Enhancement Course -SEC-5 (Discipline / Subject Specific)	2	2
	E.V.S	-	1
		18	30

Semester-IV

Part	List of Courses	Credit	No. of Hours
Part-1	Language - Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses & Elective Courses including laboratory [in Total]	16	13
Part-4	Skill Enhancement Course -SEC-6 (Discipline / Subject Specific)	2	2
	Skill Enhancement Course -SEC-7 (Discipline / Subject Specific)	2	2
	E.V.S	2	1
		28	30

Third Year Semester-V

Part	List of Courses	Credit	No. of Hours
Part-3	Core Courses including Project / Elective Based	23	26
Part-4	Value Education	2	2
	Internship / Industrial Visit / Field Visit	2	2
		26	30

Semester-VI

Part	List of Courses	Credit	No. of Hours
Part-3	Core Courses including Project / Elective Based & LAB	22	28
Part-4	Extension Activity	1	-
	Professional Competency Skill	2	2
		25	30

Consolidated Semester wise and Component wise Credit distribution

Parts	Sem I	Sem II	Sem III	Sem IV	Sem V	Sem VI	Total
							Credits
Part I	3	3	3	3	-	-	12
Part II	3	3	3	3	-	-	12
Part III	10	14	10	16	23	22	95
Part IV	2	4	2	6	4	2	20
Part V	-	-	-	-	-	1	1
Total	18	24	18	28	27	25	140

*Part I. II, and Part III components will be separately taken into account for CGPA calculation and classification for the under graduate programme and the other components. IV, V have to be completed during the duration of the programme as per the norms, to be eligible for obtaining the UG degree.

CREDIT DISTRIBUTION FOR B.Sc (Elec & Comms)

	3 – Year UG Programme ir Credits Dis		m))
	Creatis Dis	No. of Papers	Credits
Part I	Tamil(3 Credits)	4	12
Part II	English(3 Credits)	4	12
Part III	Core Courses (4 Credits)	15	95
	Elective Courses :Generic / Discipline Specific (3 Credits)	07	
	·	Total	119
Part IV	SEC1,SEC2(NME)(2 Credits)	2	
	SkillEnhancement Courses 3, 4, 5(2 Credits)	4	
	(SEC 5)EntrepreneurialSkill -1(1 Credit)	1	10
	Professional Competency Skill (2 Credits)	1	2
	EVS (2 Credits)	1	2
	Value Education (2 Credits)	1	2
	Foundation Course(2 Credits)	1	2
	Summer Internship(2 Credits)	1	2
		Part IV Credits	20
Part V	Extension Activity (NSS / NCC / Pl Education)	nysical	1
Total Cre Science	edits for the UG Programme in B.Sc	c. Computer	140

REGULATIONS

1.ELIGIBILITY:

Candidates seeking admission to the first year of the Bachelor of Science in Electronics and Communication should have passed the Higher Secondary Examination conducted by the Government of Tamil Nadu or an examination accepted as Equivalent thereto by the Syndicate subject to such conditions as may be prescribed from to time are permitted to appear and qualify for the B.Sc., Electronics and Communication Degree of this university after a course of study of Three Academic Years.

2. DURATION OF THE COURSE:

The course for the degree of Bachelor of Electronics and Communication shall consist of three academic years divided into six semesters. Each semester will be of of 90 working days.

3.COURSE OF STUDY:

The course of study shall comprise instruction in the following subjects under CBCS(CHOICE BASED CREDIT SYSTEM) pattern according to the syllabus and books prescribed from time to time.

4.FOUNDATION SUBJECTS:

PART I:Tamil / Hindi / Malayalam / French / German

PART II : English

ALLIED SUBJECTS: IN THE FIRST YEAR ONE MAJOR

Department out of the four mentioned below; to be chosen for the study of two allied papers. In the second year another major department in the list to be chosen for the study of another two allied papers.

SEMESTER	MAJOR DEPARTMENT	ALLIED SUBJECT
I / III	MATHS	ALGEBRA, CALCULUS & FOURIER SERIES
I / III	PHYSICS	ALLIED PHYSICS - I
I / III	COMPUTER SCIENCE	PROGRAMMING IN C
II / IV	MATHS	DIFFERENTIALEQUATIONS AND LAPLACE TRANSFORMS
II / IV	PHYSICS	ALLIED PHYSICS - II
II / IV	COMPUTER SCIENCE	PROGRAMMING IN VISUAL BASIC
II		ALLIED LAB - I FOR ALLIED I & II
		MATHS/PHYSICS/C & VB LAB
IV		ALLIED LAB - II FOR ALLIED III & IV
		MATHS/PHYSICS/C & VB LAB

4. SCHEME OFEXAMINATIONS

The scheme of examinations under CBCS (CHOICE BASED CREDIT SYSTEM) for different semesters shall be as follows.

		FIRST	r semeste	R			
Part	Course Code	Title of Course	Contact Hr./Week	Credit	Int. Mark	Ext. Mark	Total Mark
Ι		Tamil-I / Language@	6	3	25	75	100
II		English-I:Communicative English	6	3	25	75	100
III	23UECCT01	Core – I: Semiconductor Devices	6	5	25	75	100
III	23UECCP01	Core Practical-I: Basic Electronics Lab	3	-	-	-	-
III	23UECAT01	GEC-I: Allied I: Algebra Calculus & Fourier Series/ Programming in C/Allied Physics - I	5	5	25	75	100
	23UECAP01	Skill Enhancement Course SEC1 -Allied Lab -I Allied For I & II Sem	2	-	-	-	-
IV	23UECSO01	Skill Enhancement (Foundation Course): Applied Electric Circuits	2	2	25	75	100
			30	18	125	375	500
		SECOND S	SEMESTER				
Part	Course Code	Title of Course	Contact Hr./Week	Credit	Int. Mark	Ext. Mark	Total Mark
Ι		Tamil-II / Language@	6	3	25	75	100
II		English-II: Communicative English	4	3	25	75	100
II	NMSDC	Language Proficiency for Employability-Overview of English Communication	2	2	25	75	100
III	23UECCT02	Core – II: Applied Digital Electronics	6	5	25	75	100
III	23UECCP01	Core Practical – I: Basic Electronics Lab	3	4	40	60	100
III	23UECAT02	GEC-II: Allied II: Differential Equations and Laplace Transforms/ Programming in Visual Basic/Allied Physics II.	5	5	25	75	100
	23UECAP01	Skill Enhancement Course –SEC1- Allied Lab -I Allied For I & II Sem	2	2	40	60	100

	23UECSO02	Skill Enhancement Course					
IV	250EC5002	-SEC-2: Power Electronics	2	2	25	75	100
			30	26	205	495	700
		THIRD S	EMESTER	•			
Part	Course Code	Title of Course	Contact Hr./Week	Credit	Int. Mark	Ext. Mark	Total Mark
Ι		Tamil-III / Language@	6	3	25	75	100
II		English-III	6	3	25	75	100
III	23UECCT03	Core – III: Electronic Circuits	5	5	25	75	100
III	23UECCP02	Core Practical – II: Electronic Circuits Lab	3	-	-	-	-
III	23UECCP03	Core Practical – III: 8085 Microprocessor and Interfacing Lab	-	-	-	-	-
III	23UECAT03	GEC-III: Allied III: Algebra Calculus & Fourier Series/ Programming in C/Allied Physics - I	5	5	25	75	100
III	23UECAP02	Skill Enhancement Course -SEC-3 (Entrepreneurial Skills): Allied Lab -II Allied For III& IV Sem	2	-	-	-	-
IV	NMSDC	Digital Skills for Employability-Digital Skills	2	2	25	75	100
IV		Environmental Studies (EVS)	1	-	-	-	-
			30	18	125	375	500
		FOURTH	SEMESTER				
Part	Course Code	Title of Course	Contact	Credit	Int.	Ext.	Total
			Hr./Week		Mark	Mark	Mark
Ι		Tamil-IV/ Language@	6	3	25	75	100
II		English-IV	6	3	25	75	100
III	23UECCT04	Core – IV: 8085 Microprocessor and Interfacing	5	4	25	75	100
III	23UECCP02	Core Practical – II: Electronic Circuits Lab	-	4	40	60	100
III	23UECCP03	Core Practical – III: 8085 Microprocessor and Interfacing Lab	3	4	40	60	100
III	23UECAT04	GEC-IV: Allied IV: Differential Equations and LaplaceTransforms/Progra mming in Visual Basic/Allied Physics II.	5	4	25	75	100

1 al l			Hr./Week		Mark	Mark	Mark
Part	Course Code	Title of Course	Contact	Credit	Int.	Ext.	Total
		SIXTH S	EMESTER	•			
			30	27			700
		Training!*	summer holidays				
IV	23UECSI01	Summer Internship/Industrial	Minimum 15 Days during	2	40	60	100
IV	23UECVE01	Value Education	2	2	25	75	100
III	23UECCP05	Core Practical – V: From Group D (8051 Microcontroller & Interfacing Lab/16F877 Microcontroller & Interfacing Lab/ PLC Programming Lab)	3	-	-	-	-
III	23UECCP04	Core Practical – IV: IC's & Communication Lab	3	-	-	-	-
III	23UECME01	Elective I/GEC: From Group A:8051 Microcontroller &Interfacing/ PIC16F877 Microcontroller & Interfacing/Programmable Logic Controllers	4	3	25	75	100
III	23UECCT08	Core – VIII: Biomedical Instruments (or) Project with viva voce*	4	5	25/VIVA 20	75/eva luatio n 80	100
III	23UECCT07	Core – VII: Electronic Instrumentation	4	5	25	75	100
III	23UECCT06	Communication Systems Core – VI: IC's & Their Applications	5	5	25	75	100
III	23UECCT05	Core – V: Electronic	Hr./Week 5	5	Mark 25	Mark 75	Mark 100
Part	Course Code	Title of Course	Contact	Credit	Int.	Ext.	Total
		FIFTH SI	EMESTER				
			30	28	270	630	900
IV		Environmental Studies	1	2	25	75	100
IV		Skill Enhancement Course -SEC-5: NMEC #\$	2	2	25	75	100
III	23UECAP02	Skill Enhancement Course -SEC-3): Allied Lab -II Allied For III& IV Sem	2	2	40	60	100

							1
III	23UECCT09	Core – IX: PC Hardware Networking & Troubleshooting	6	4	25	75	100
III	23UECCT10	Core – X: Network Communication & Security	6	4	25	75	100
III	23UECME02	Elective II: From Group B: Modern Television Systems/Radar and Navigational Systems/Satellite, Cable and DTH Systems/Mobile Communication & Servicing.	5	3	25	75	100
III	23UECME03	Elective III/GEC: From Group C (PCB Design and Fabrication/Programming using Verilog HDL/Electronic Defense Systems/Nano Electronics	5	3	25	75	100
III	23UECCP04	Core Practical – IV: IC's & Communication Lab	3	4	40	60	100
III	23UECCP05	Core Practical – V: From Group D (8051 Microcontroller & Interfacing Lab/16F877 Microcontroller & Interfacing Lab/ PLC Programming Lab)	3	4	40	60	100
IV		Professional Competency Skill: COMPETITIVE SKILLS	2	2	25	75	100
V		Extension Activity	-	1	-	-	-
		Total Credits & Marks No Internal Mark for Project Work Project Report Evaluation Viva Voce Evaluation	30 80 Marks 20 Marks	25 140	205	495	700 4000

!*Internship: To be carried out 4th semester vacation period ,The internship course will not meet as a regular class. Minimum 15 Days during summer holidays.

@Anyother Language like Hindi/Malayalam/French/etc.

#\$Those who have not studied Tamil up toXII std and taken a Non-Tamil language under Part-I shall take Tamil comprising of two courses (level will beat 6thstandard)instead of NMEC

#\$Those who have studied Tamil up to XII std and taken a Non-Tamil language under Part-I shall take Advanced Tamil comprising of two courses instead of NMEC.

4. A. INTERNSHIPS OBJECTIVES

The internship could be a new job or a new experience within an existing job as the work performed in the internship should lead to new learning, discovery or growth for the student and contribute toward the student's academic program.

The student intern will either find a company to sponsor him or her or perform a specific project at an existing job. The internship program is an agreement between the student, faculty advisor and the company supervisor.

Perform job requirements in a professional manner.

Maintain regular and prompt attendance, as agreed upon with supervisor. The submission of report is written at the end of the internship by the student to the institute which form part in the awarding of the degree certificate.

INTERNSHIPS OUTCOME

An internship provides the student the opportunity to build upon, apply, and assess the concepts that are developed through the College's curriculum and to further the student's professional growth through a meaningful –real-world job experience. Internships.

5. QUESTION PAPER PATTERN FOR UG ELECTRONICS AND COMMUNICATION

COURSES MARK DISTRIBUTION FOR THEORY (EXTERNAL)

TIME :3 HOURS

MAXIMUM MARKS: 75 PASSING MI

PASSING MINIMUM: 30MARKS

Part A 15x1=15

(Answer All Questions)

(Three Questions from Each Unit)

Part B 5x2=10

(Answer Any Two Questions)(One Questions from Each Unit)

Part C 5x10=50 (Answer All Questions) (One Question from Each Unit with Internal Choice)

Mark Distribution for Theory (Internal)

Total	:	25
Subject Level Attendance	:	5
Subject Level Assignment	:	5
Subject Level Cycle Test and Model Exam	:	15
Max. Marks: 25		

PassingMinimum:10 Marks

MARK DISTRIBUTION FOR PRACTICALS (EXTERNAL)

TIME: 3 HOURS; MAXIMUM MARKS : 60; PASSING MINIMUM : 24 MARKS

PRACTICAL EXAM : 50 MARKS & RECORD : 10 MARKS

Submission of Record Note Books for Practical Exams: Candidates appearing for the Practical Exams must submit Bonafide Record Note Book, otherwise the candidate will not be permitted to appear for the practical exam.

Mark Distribution for Practical (Internal)

Total	40
Year Through; Lab Attendance	: 10
Model Practical's:	20
Year Through; Lab Performance	: 10
Max. Marks: 40	

Passing Minimum: 16 Marks

16

6. PROJECT WORK OR ONE CORE PAPER:

A Candidate can submit **SOFTWARE or HARDWARE or HARDWARE cum SOFTWARE** based project and has to demonstrate the Project with Project Report in the University Project Viva Voce Examination conducted at the end of the sixth semester.

MARK DISTRIBUTION FOR PROJECT REPORT:80 MARKS

MARK DISTRIBUTION FOR VIVAVOCE : 20 MARKS

Instead of Project Work; Core Paper - VIII "BIOMEDICAL INSTRUMENTS" may be

offered in the Vth Semester.

7. PASSING MINIMUM:

A candidate shall be declared to have passed the examination only if the candidate secures a minimum of 40% in the University examination and with an overall total of 40 out of 100.

8. RESTRICTIONS TO APPEAR FOR THE EXAMINATIONS

Candidates who fail in any of the course of PartI, II,III,IV &PartV of UG Degree examinations shall complete the course concerned within 5 years from the date of admission to the said programme and should they fail to do so, they shall take the examination in the revised syllabus prescribed for the immediate next batch of candidates.

If there is no change in the syllabus they shall appear for the examination in that course with the syllabus in vogue until there is a change in the syllabus. In the event of removal of that course consequent to change of regulation and/or curriculum after5-year period, the candidates shall have to take up an equivalent course in the revised syllabus as suggested by the Chairman and fulfill the requirements as per the regulation curriculum for the award of the degree.

9. IMPROVEMENT OF MARKS IN THE SUBJECTS ALREADY PASSED

Candidates desirous of improving the marks awarded in a passed subject in their first attempt shall reappear once with in a period of subsequent two semesters. The improved marks shall be considered for classification but not for ranking. When there is no improvement, there shall not be any change in the original marks already awarded.

10. CLASSIFICATION OF SUCCESSFUL CANDIDATES

A candidate who passes all the examinations in Part I to Part V securing following CGPA and Grades shall be declared as follows for Part I or Part II or Part III.

CGPA	GRADE	CLASSIFICATION OF FINAL RESULT
9.5 - 10.0	O +	First Class -Exemplary
9.0 and above but below 9.5	0	
8.5 and above but below 9.0	D++	
8.0 and above but below 8.5	D+	First Class with Distinction
7.5 and above but below 8.0	D	
7.0 and above but below 7.5	A++	
6.5 and above but below 7.0	A+	First Class

		_
6.0 and above but below 6.5	А	
5.5 and above but below 6.0	B+	Second Class
5.0 and above but below 5.5	В	
4.5 and above but below 5.0	C+	Third Class
4.0 and above but below 4.5	С	

11. RANKING

A candidate who qualifies for the UG degree course passing all the examinations in the first attempt, within the Minimum period prescribed for the course of study from the date of admission to the course and secures I or II class shall be eligible for ranking and such ranking shall be confined to10% of the total number of candidates qualified in that particular branch of study; subject to a maximum of 10 Ranks. The improved marks shall not be taken into consideration for ranking.

12. COMMENCEMENT OF THIS REGULATION:

These regulations shall take effect from the academic year 2023-2024, i.e., for students who are to be admitted to the first year of the course during the academic year 2023-2024 and thereafter.

13. COURSE EQUIVALENCE:

The Three Year Course in the Bachelor of Science in Electronics & Communication is Equivalent to :

- · B.Sc.Electronics
- · B.Sc. Industrial Electronics
- B.E.S.(Bachelor of Electronic Science)

14. ACADEMIC OPPORTUNITIES:

After successful completion of this course ; students can pursue higher degree courses like M.Sc., (Electronics) / M.Sc., (Electronic Science) / M.Sc., (Applied Electronics) / M.Sc., (Electronics & Instrumentation) / M.Sc., (Cyber Forensics & Information Security) M.Sc., (Criminology & Criminal Justice Science) / M.Sc., (Computer Science) / MCA / M.Sc., (Nano Science and Technology) / M.Sc., HRD Psychology / M.Sc., (Energy Science) / M.Sc., Environmental Science)/M.Sc., Library & Information Science)/M.Sc., YOGA/MBA/MA., (Defense & Strategic Studies/MA., Yoga/MA., Public Administration /MA., Political Science etc.

15. JOB OPPORTUNITIES:

The Students are eligible for placement in Army,Navy,Airforce, Civil Aviation Sectors, Space & Radio Astronomy Sectors, T.V. Broad Casting Stations, All India Radio, BSNL,TNEB, ITES (IT Enabled Software Services), Telecommunication Sectors, Banking and Railway Services and as Computer Hardware & Instrument Service Professionals. A Wide variety of Self – Employment Opportunities are also available.

B.SC. ELECTRONICS AND COMMUNICATION

L	Т	Р	С
6	0	0	5

SEMESTER I

CORE I - SEMICONDUCTOR DEVICES

COURSE OBJECTIVES:

This course provides the important aspect of semiconductors and devices like diodes, Transistor, JFET and MOSFET.

Course	Outcomes: After completion of this course, student able to
CO1	Understand the band diagram, type of semiconductors and charge carrier life time.
CO2	Understand of capacitors and types of diodes breakdown
CO3	Demonstrate the transistor and their types
CO4	Understand the fundamentals for construction of different type of JFET.
CO5	To study the operating principles of MOSFET.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Atom	Structure of Atom - Atomic Number - Valence Electrons - Bonding in Conductors - Insulators - Semiconductors - Energy Band Diagram of Conductors - Insulators - Semiconductors - Intrinsic Semiconductor - Extrinsic Semiconductor - P Type Semiconductor - N type Semiconductor - Carrier Life Time.	10
II	PN Junction Diode	Theory of PN Junction Diode - Energy Band Structure - Diode Current Equation - Diode Resistance - Depletion Capacitance - Diffusion Capacitance - Effect of Temperature - PN Junction Diode as a Rectifier - Zener Diode -	10

PE	PERIYAR UNIVERSITY				
		Avalanche Break Down - Zener Break Down - Zener Diode as a Voltage Regulator.			
III	Transistor	Operation of PNP & NPN Transistor - CB, CE, CC Configuration and Characteristics - Transistor as an Amplifier.	10		
IV	JFET	Construction - Operation - Output & Transfer Characteristics of P Channel & N Channel JFET - Characteristic Parameters of the JFET - Biasing the FET - Comparison of JFET & BJT - Comparison of P Channel & N Channel JFET - Applications of JFET - JFET as a Voltage Variable Resistor.	10		
v	MOSFET	Construction, Operation, Output & Transfer Characteristics of P Channel & N Channel Depletion MOSFET - Construction, Operation, Output & Transfer Characteristics P Channel & N Channel Enhancement MOSFET - Biasing the MOSFET - Comparison of P Channel MOSFET with N Channel MOSFET - Comparison of JFET with MOSFET - Handling Precautions for the MOSFET.	10		

REFERENCE BOOKS:

- 1. ElectronicDevices&Circuits-Salivahanan-TMH-2ndEdition
- 2. ATextBookofAppliedElectronics-R.S.Sedha-S.Chand-Rs.395

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTR - I

SKILL ENHANCEMENT

(Foundation Course)

APPLIED ELECTRIC CIRCUITS

COURSE OBJECTIVES:

- To understand and acquire knowledge about various circuit theorem.
- To make students strong in electric circuit designing.
- To learn the different laws and implement in circuits and also compare its gain.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Examine the basic components in series and parallel connection.
CO2	Have a thorough understanding of the different circuit laws.
CO3	Understand various circuit theorems.
CO4	Analyse RMS & Power parameters of AC circuits.
CO5	Acquire the knowledge of RLC and its applications.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Circuit components	Resistors, Capacitors & Inductors in Series and Parallel - Factors governing the Resistance of a Resistor, Capacitor & Inductor - Colour Coding of Resistors - Energy Stored in a Capacitor - Energy Stored in an Inductor – Various Other Passive & Active Devices -	

	ERITAR UNIV		
		Simple Problems.	
II	Cinquit lang	Ohms Law - Kirchhoff's Voltage Law - Kirchhoff's Current Law - Current Division - Voltage Division -	05
	Circuit laws	Star Connection - Delta Connection - Series Circuits - Parallel Circuits - Series & Parallel Circuits - Open Circuit - Short Circuit - Simple Problems.	03
III	Theorems	Super Position Theorem – Thevenin's Theorem – Norton's Theorem – Millman's Theorem Maximum Power Transfer Theorem – Simple Problems	05
IV	Ac circuit basics	Sinusoidal and Non Sinusoidal Waveforms – Peak Value – Peak to Peak Value – Average Value – RMS Value – Period and Frequency Measurement - Power Factor - Real Power – Reactive Power – Simple Problems.	05
V	Reactance, impedance & resonance	Capacitive Reactance – Inductive Reactance – Impedance – RL and RC in Series and Parallel – RLC in Series and Parallel – Series Resonance - Parallel Resonance - Simple Problems	05

Books for Reference

- 1. Circuits and Networks: Analysis and Synthesis Sudhakar &
- Shyam Mohan TMH IV Edition
- 2. Basic Electronics Bernard Grob Mcgraw Hill.
- 3. Circuit Theory Salivahanan S.Chand Rs.395/-

B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER II

CORE II - APPLIED DIGITAL ELECTRONICS

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COURSE OBJECTIVES:

- To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronic circuits.
- To prepare students to perform the analysis and design of various digital electronic circuits.
- To learn the design process of registers, counters and conversion of analog to digital conversion and vice-versa.

	NSE OUTCOMES: After completion of this course, student able to
CO1	Examine the structure of various number systems and its application in digital
	design.
CO2	Have a thorough understanding of the fundamental concepts and minimization
	techniques used in digital electronics.
CO3	Understand various combinational logic circuits and its applications
CO4	Analyse and design various sequential logic circuits and its applications.
CO5	Analyze the logic levels and apply them for the design of analog to digital
	conversion and vice versa.

COURSE OUTCOMES: After completion of this course, student able to

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Number	Binary Signals – Binary Number System – Decimal	
	systems	Number System - Octal Number System – Hexadecimal	10
		Number System – Conversion from One Number	
		System to Another Number System - BCD – Gray code	
		– Excess – 3 Code – ASCII code.	
II	Boolean	Binary Addition, Subtraction, Multiplication &	
	algebra	Division - 1's and 2's Complement Subtraction - 9's &	
		10's Complement Subtraction - Basic laws of Boolean	10
		Algebra - Duality Theorem - De Morgan's Theorem -	
		Sum of Products – Product of Sum - Two Variable,	
		Three Variable & Four Variable Karnaugh Maps .	
III	Combination	Logic Gates: AND, OR, NOT, EX-OR, EX-NOR,	
	al Elements	NAND & NOR - Logic Gates using Discrete	
		Components - NAND & NOR as Universal Gates - Half	10
		& Full Adder – Half & Full Subtractor - Encoder -	
		Decoder - Multiplexer - Demultiplexer -	
		Implementation using 74147, 7442, 74153 & 74155	
		IC's.	
IV	Sequential	Flip Flops: RS - Clocked RS - JK - Master Slave JK - D	
	Elements	& T Flip Flops – Shift Registers: SIPO – SISO – PIPO	
		– PISO – Shift Left – Shift Right - Ring counter –	10
		Twisted Ring Counter. Counters: Hexadecimal Up -	
		Hexadecimal Down - Modulo Up - Modulo Down -	
		UP/DOWN Counters - Implementation Using 7476,	
1			

PE	PERIYAR UNIVERSITY				
		7495, 7493 & 7490 IC's.			
V	A/D AND D/A Conversion	Parallel Comparator Type of ADC - Counter Ramp Type of ADC - Successive Approximation Type of			
	Conversion	ADC - Dual Slope Type of ADC - ADC Accuracy and Resolution - Binary weighted Resistor type of DAC -	10		
		R-2R Ladder Type of DAC - DAC Accuracy and			
		Resolution - Implementation using ADC 0809 & DAC 0800 IC's.			

Books for study

- 1. Anand Kumar. 2008. Fundamentals of Digital Circuits. [Second Edition]. PHI.
- 2. Salivahanan. 2004. Digital Circuits and Design. [Fourth Edition]. S.Chand.

Books for Reference

- Donald, P. Leach, Albert Paul Malvino and Goutam Saha. 2008. Digital Principles and Applications. [Sixth Edition]. Tata Mc Graw Hill, New Delhi.
- Virendra Kumar. 2009. Digital Technology Principles and Practice. [First Edition]. New Age International Publications, New Delhi.
- Jacob Millman and Christos Halkias. 2011. Integrated Electronics Analog and Digital Circuits and Systems. [Second Edition]. Tata Mc Graw Hill Publishing Company Limited, New Delhi.
- 4. *Thomas, L.Floyd.* 2006. **Digital Fundamentals.** [Ninth Edition]. Pearson Education, New Delhi.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER II

SKILL ENHANCEMENT COURSE

SEC-2- POWER ELECTRONICS

COURSE OBJECTIVES:

- To understand and acquire knowledge about various power semiconductor devices.
- To provide the students a deep insight in to the working of different switching devices with respect to their characteristics.
- To study the principle of operation, design and synthesis of different types of power supplies and their applications.

COURSE OUTCOMES: After completion of this course, student able to
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CO1	Ability to express characteristics of SCR, TRIAC, DIAC and UJT
CO2	Acquire knowledge about fundamental concepts and methods to turn ON and
	turn OFF the thyristor.
CO3	Understand the design principle of triggering circuit of SCR
CO4	Explain the switching principle and applications
CO5	Remember the working principle of various types of power supplies.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Thyristor	Simple Theory & Characteristics of SCR - DIAC,	
	Characteristi cs	TRIAC - UJT. UJT as an Oscillator.	05

PI	PERIYAR UNIVERSITY		
II	Methods of Turn ON & Turn OFF	AC gate Triggering - R Triggering - RC Triggering - DC gate Triggering - Pulse gate Triggering – Natural Commutation - Force Commutation – Self - Impulse - Resonant - Complementary - External -Load side - Line Side.	05
III	Triggering SCR	Triggering of series connected SCR's - Triggering of Parallel Connected SCR's – Current & Voltage Protection - Snubber Circuit.	05
IV	Static Switches	Single Phase AC Switches - Three Phase AC Switches - Three Phase Reversing Switches - AC Switches for Bus Transfer - DC Switches - Solid State Relays.	05
V	Power Supplies	Switched mode DC Power Supplies - Resonant DC Power Supplies - Bidirectional Power Supplies - Switched mode AC Power Supplies - Resonant AC Power Supplies - Bidirectional AC Power Supplies.	05

Books for study

- 1. Power Electronics Muhammed H. Rashid PHI 2nd Edition
- 2. Power Electronics Jaganathan PHI II Edition.

Books for Reference

- 1. *Singh M D and Khanchandani K B*,2007, **Power electronics** [Second Edition], Tata Mcgraw hill, Newdelhi.
- 2. *Mithal.G.K,2000*, **Industrial electronics and control** [Eighteenth Edition],Tata Mcgraw hill, Newdelhi
- 3. *Theraja B. L, Theraja.A.K, 2003*, **Electrical Technology** [First Edition], S.Chand, Newdelhi.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER I & II

CORE PRACTICAL I - BASIC ELECTRONICS LAB

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

COURSE OBJECTIVES:

Basic electronics laboratory is the gateway of the electronics & communication engineering world. At the very beginning the students get familiarized with the various electronics instruments & components which basically equip them to construct complex circuits in near future. In this lab students build up preliminary electronic circuits and verify the results with theoretical concepts. Lab experiments are designed in such a fashion that the engineering skill of the students starts to enrich.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Ability to express characteristics of DIODES, BJT, THYRISTORS	
CO2	To understand the basic theorems and to verify their operation	
CO3	To understand the basic digital circuits and to verify their operation	
CO4	Learn the basics of gates & construct basic combinational circuits and verify their	
	functionalities	
CO5	Learn about Shift registers & counters	

<u>Syllabus</u>

- 1. Color Coding of Resistors
- 2. PNJunctionDiodeCharacteristics.
- 3. ZenerDiodeCharacteristics.
- 4. CEInputCharacteristics.

- 5. CEOutputCharacteristics.
- 6. SCR/TRIA Characteristics.
- 7. DC Regulated Power Supply Using Zener Diode.
- 8. Verification of Ohm's Law.
- 9. Verification of Kirchhoff sCurrentLaw.
- 10. VerificationofKirchhoff's Voltage law.
- 11. VerificationofThevenin'sTheorem/Norton's Theorem.
- 12. Verification of Millman's Theorem
- 13. VerificationofMaximumPower Transfer Theorem
- 14. InductanceCalculationUsingSeriesor/Parallel Resonance.
- 15. TransientResponseofanRCCircuit.
- 16. Truth Table VerificationofBasic Gates (AnyTwo)
- 17. LogicGates Using Discrete Components (AnyOne).
- 18. +5VRegulatedPowerSupply.
- 19. NANDorNORasaUniversal Gate (AnyOneGate).
- 20. VerificationofDeMorgan'sTheorem.
- 21. Truth Table Verification of Half Adder & Full Adder
- 22. Truth TableVerificationofHalf Subtractor &Full Subtractor.
- 23. EncoderUsing74147IC
- 24. DecoderUsing7442IC
- 25. MultiplexerUsing74153IC
- 26. DemultiplexerUsing74155IC
- 27. MSJKFlipFlopUsing7476IC
- 28. ParallelInParallelOutShiftRegisterUsing7495IC
- 29. UpCounterUsing7490ICor7493IC.
- 30. Clock Generation Using NAND or Nor Gate.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER III

CORE III - ELECTRONIC CIRCUITS

COURSE OBJECTIVES:

To analyze and describe the applied electronics principles used to develop circuits and systems& To understand the fundamentals of the alternating current. To apply the basic tools and test equipment used to construct, troubleshoot, and design standard electronic circuits.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the basics of electrical energy and practical implementation of electrical	
	fundamentals.	
CO2	solve design problems on rectifiers, filters and power supply circuits.	
CO3	Understand various types of amplifier.	
CO4	Examine the basic components of feedback &its types.	
CO5	Acquire the knowledge about oscillators, Multivibrators and Wave shaping circuits.	

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Power supply's	HalfWaveRectifier-FullWaveRectifier- BridgeRectifier-Averagevalue-RMSvalue- Formfactor-Peak factor – Ripple factor – Efficiency – TUF – PIV – Filters: C, L, LC, CLC, CRC – Voltage Regulators:SeriesRegulators- ShuntRegulators- ICVoltageRegulators(78XX&79XX)	10

II	Transistor biasing	Bias Stability – Thermal runaway – Methods of transistor Biasing – Bias compensation – Wave Shaping Circuits – RC & RL Circuits – Clipping & Clamping Circuits – Voltage Doubler – Tripler – Quadrupler.	10
ш	Types of amplifiers	Class A Amplifier – Class B Amplifier – Class AB Amplifier – Push Pull Amplifier – Complementary symmetry Push Pull Amplifier – Class C Amplifier – Multistage Amplifiers: RC Coupled Amplifier – Transformer Coupled Amplifier – Direct Coupled Amplifier.	10
IV	Feedback	Basics concepts of Feedback – Effects of negative feedback on gain, Bandwidth, Distortion, Noise, Input ImpedanceandOutputImpedanceTypesofNegativeFeedb ack-Voltage Series-Voltage Shunt-Current SeriesandCurrentShuntFeedback	10
v	Oscillators	Ators ClassificationofOscillators-BarkhausenCriterion- HartleyOscillator–Colpitt'sOscillator-ClappOscillator - Phase Shift Oscillator – Wein Bridge – Crystal Oscillator – Frequency stability of Oscillators – Astable Multivibrator - Monostable Multivibrator Bistable Multivibrator - Schmitt Trigger.	

REFERENCE BOOKS:

- 1. ElectronicDevices&Circuits-S.Salivahanan-TMH-IIEdition
- 2. ATextBookofElectronicDevices&Circuits-R.S.Sedha-S.ChandRs.325/-

B.SC. ELECTRONICS AND COMMUNICATION

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SEMESTER III

SKILL ENHANCEMENT COURSE-SEC4

NONMAJOR ELECTIVE COURSE- I

GROUP A - PAPER I -BASIC ELECTRONICS- I

COURSE OBJECTIVES:

This course provides the important aspect of semiconductors and devices like diodes, Transistor, JFET and MOSFET &to understand and acquire knowledge about various circuit theorem, make students strong in electric circuit designing.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the fundamentals for construction & different type of Diode,BJT.
CO2	Understand the fundamentals for construction of different type of JFET, MOS, etc
CO3	Understand various circuit Laws.
CO4	Examine the basic components in series and parallel connection.
CO5	Analyse RMS & Power parameters of AC circuits.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Semiconduct or theory	Intrinsic Semiconductor – Extrinsic Semiconductor – Theory of PN Junction Diode - Zener Diode – Avalanche Breakdown - Zener Break Down – Operation of PNP & NPN Transistor - CB, CE, CC Configuration and Characteristics - Transistor as an Amplifier.	05

II	Electronic components	Simple Theory & Use of: Resistors, Capacitors, Inductors, Diodes, Zener diodes, Transistors, FET, MOSFET, UJT, SCR, DIAC, TRIAC, LED, Seven Segment Display, Basic Gate IC's, Transformers, LDR, Switches, Microphone, Loudspeaker, Buzzers, Fuse.	05
ш	Circuit laws	Ohm'sLaw-Kirchhoff [*] sCurrentLaw- Kirchhoff [*] sVoltageLaw-VoltageDivision- CurrentDivision-Series Circuits-ParallelCircuits- Series&ParallelCircuits-OpenCircuit-ShortCircuit.	05
IV	Resistors, capacitors & inductors	Resistor Resistors, Capacitors & Inductors in Series and Parallel-Factors governing the Resistance of a ,Capacitor&Inductor-ColourCodingofResistors- EnergyStoredinaCapacitor-EnergyStoredin anInductor	05
V	Waveforms	SinusoidalWaveform-Non-SinusoidalWaveforms- PeakValue-PeaktoPeakValue-AverageValue-RMS Value–Period&FrequencyMeasurement.	05

REFERENCE BOOKS:

- 1. ElectronicDevices&Circuits-Salivahanan-TMH-2ndEdition
- 2. PrinciplesofElectronics-V.K.Mehta-S.Chand.
- 3. Circuits&Networks-Sudhakar-TMH-4thEdition.

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SEMESTER III

SKILL ENHANCEMENT COURSE-SEC4

NON-MAJOR ELECTIVE COURSE I

GROUP A PAPER II -BIO MEDICAL ELECTRONICS – I

COURSE OBJECTIVES:

- To Illustrate origin of bio potentials and its propagations
- To understand the different types of electrodes and its placement for various recordings
- To understand various physiological recordings and its measurements
- To learn the medical imaging equipment and techniques for surgeries.
- To summarize therapeutic equipment and Patient monitoring techniques for biological operations.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Differentiate different bio potentials and its propagations.
CO2	Illustrate different electrodes and its placement for bio potentials recordings and measurements
CO3	Demonstrate various electrophysiological recordings and measurements
CO4	Explain medical imaging equipment's using in surgeries
CO5	Demonstrate different therapeutic equipment's and Patient monitoring techniques

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
I	Introduction	Introduction to Human Physiology – Micro Electrodes – Skin Surface Electrodes – Needle Electrodes – Reference Electrodes.	05

<u>P</u>	PERIYAR UNIVERSITY				
II	Meters & recorders	Digital Thermometer – Sphygmomanometer - Electronic Sthethoscope – ECG - EEG – EMG	05		
ш	Cardio Tocography	Cardiotocography - Electro Oculography - Electro Retinography - Poly Somnography Blood Flow Meter - Doppler – Audiometer	05		
IV	Operation theatre equipment's	UpperEndoscope-LowerEndoscope-ENTEndoscope	05		
V	Diathermy	Diathermy - Surgical Diathermy- Micro Wave Diathermy - Multipara Patient Monitor.	05		

- 1. BiomedicalInstrumentation&Measurements-AnandaNatarajan-PHI-Rs.275/-
- 2. BiomedicalInstrumentationandMeasurements-LeslieCromwell-PHI-2ndEdition.
- 3. Bio-MedicalInstrumentation-Dr.M.Arumugam-AnuradhaAgencies-2ndEdition
- 4. HandbookofBiomedicalInstrumentation-R.S.Khandpur-TMH
- 5. MedicalInstrumentation,ApplicationAndDesign–JohnG.Webster–WEL-3rdEdition
- 6. ATextofBookofMedicalInstruments-Ananthi-NewAgeInternational-Rs.275/-
- 7. Internet :AdditionalReferenceForAllUnits.

B.SC. ELECTRONICS AND COMMUNICATION

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SEMESTER III

SKILL ENHANCEMENT COURSE-SEC4

NON-MAJOR ELECTIVE COURSE I GROUP A PAPER III - CELLULAR PHONES

COURSE OBJECTIVES:

To get knowledge about Cellular Radio, Elements of a Cellular Network, Cellular Telephony, Radio Propagation and its applications & the mobile servicing and Software Repairing.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the fundamental concepts of Cellular.
CO2	Understand the different accessing technology.
CO3	Understand the hardware & software of mobile
CO4	Understand the servicing of mobile
CO5	Understand the other mobile service tools

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Basics	WorkingofaTelephone-LocalExchange- Initiatingacall-CallingaNumber-MakingaConnection- AnsweringaCall-Conversation-EndingaCall- HookSwitch-Transmitter-Receiver-Ringer-Cellular Mobile Telephone System-Mobile Phone Service Area-Mobile Fraud Call.	05

PI	ERIYAR UNIV	ERSITY	
II	Access technologies	GSM-CDMA-GPRS-EDGE-WCDMA-UMTS- HSDPA-SatellitePhones-GPS-MobileBrowsers- WAP.	05
III	Hardware/S oftware	Types of: Wireless Options, Batteries, Memory Cards, Messaging, Ring Tones, Keypad Types, Display Types. Handset Form Factor - SMS Abbreviations - Mobile OS.	05
IV	Hardware/so ftware repairing	Hardware/Software Repairing - Various Locks - Installation of: UFS Driver, UFS Suite & Flashing Files - IMEI Number Detection - Mobile GSM Utility Codes (Any Five of Nokia Set)	05
V	Other mobile service tools	UltrasonicCleaner-ComputerConnectors- SIMCardReader-MemoryCardReader-MobileVirus- Virus Prevention-Removing Virus-Health Hazards with Mobiles-SAR.	05

- 1. ModernMobilePhoneIntroduction&Servicing-ManaharLotia-BPB-Rs.75/-(Unit-I)
- Modern Mobile Phone Repair Using Computer Software & Service Devices -ManaharLotia

 BPB - 120/- (Units I, IV & V)
- 3. ModernMobilePhoneUnlocking&UtilityCodesForGSM&CDMAPhones-ManaharLotia-BPB- Rs.99/-(Unit-IV).
- 4. Mobile Telephony Digit Magazine Supplement Jan 2006 Jasubhai Digital Media Publications. (UnitII&III)
- 5. BlueToothTechnology-CSRPrabhu&APrathapReddi-PHI-Rs.250/-
- 6. Mobile&PersonalCommunicationSystems&Services-RajPandya-PHI-Rs.250/-
- 7. INTERNET: ADDITIONAL REFERENCE FOR ALLUNITS.

B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER IV

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CORE IV - 8085 MICROPROCESSOR & INTERFACING

COURSE OBJECTIVES:

To understand the features and applications of 8085 microprocessor & exploit the abilities for the designand peripheral interfacing with real time domains

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the evolution of processor and 8085 architectures.
CO2	Learn the instruction and to create the ALP.
CO3	Investigate the instruction format and machine cycle fetch.
CO4	Compute and design for time delay and counters.
CO5	Design and development of interfacing and applications.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	8085 Microproces sor	Signals on 8085 – Architecture of 8085 – Demultiplexing the Bus - Generating Control Signals – Fetching, Decoding and Execution of an Instruction– Memory Mapping fora8K Memory Chip -StudyofEPROM(2764)&StudyofRWM(6264).	10
II	Instruction set	Data Transfer – Arithmetic – Logical – Branching - Machine Control Instructions - Stack & Stack Operations - Simple Programs.	10

Π	TimingDiagr am	Addressing Modes –Instruction Format-Memory Read Machine Cycle (MOV C,A &MVIA,32)- Memory WriteMachineCycle(MVIM,48)- TimingDiagramofIN&OUTInstruction- InterruptSystemof8085.	10
IV	Peripherals	Time Delay Program – Signals on 8255 – I/O Mode – BSR Mode - Interfacing With: Switches &MatrixKeyboard.	10
V	Interfacing	Interfacing With: LED'S - Single & Multiple Seven Segment Displays - LCD - ADC 0809 - DAC 0800 – Stepper Motor - Traffic Light Control System.	10

- 1. Microprocessor Architecture, Programming and Applications With the 8085/8080A Ramesh.S Gaonkar-NewAgeInternational–5thEdition.
- 2. IntroductiontoMicroprocessor-APMathur-TMH–3rdEdition
- 3. Microprocessor and its Applications S. Malarvizhi– Anuradha Publications
- 4. FundamentalsofMicroprocessorsandMicroControllers–B.Ram–DhanpatRai– IVthEdition

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER III & IV

CORE PRACTICAL II - ELECTRONIC CIRCUITS LAB

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

Course Objectives

The objective of the course is to equip the students with in-depth basic concepts and understanding of the principles of operation, construction, and characteristics of basic electronic equipment, and their utilization in basic electronics building blocks (or modules) and their performances practically. The techniques of analysis and design of basic building blocks of modern technology using device would be emphasized.

COURSE OUTCOMES: After completion of this course, student able to

CO1	To develop hands-on skills and knowledge about the electronic devices such as oscilloscopes, function generators, multimeter, etc.
CO2	To implement different types of electronic circuits using the techniques, skills.
CO3	To analyze complex networks of resistors, inductors, capacitors subject to both direct (non-time-varying) and alternating voltages and currents.

Syllabus

- 1. Amplitude and Frequency Measurement Using CRO
- 2. Half wave Rectifier with Capacitor Filter
- 3. Full Wave RectifierwithCapacitorFilter
- 4. Bridge Rectifier with Capacitor Filter
- 5. Regulated Dual Power Supply Using 78XX&79XX
- 6. Positive & Negative Clipping at 0V

- 7. Positive & Negative Clamping at 0V
- 8. Voltage Doubler/Voltage Tripler/Voltage Quadrupler
- 9. Basic Differentiator & Basic Integrator
- 10. Hartley Oscillator Using Transistor
- 11. Colpitt's Oscillator or Clapp Oscillator Using Transistor.
- 12. Phase Shift/Wein Bridge Oscillator Using Transistor.
- 13. Crystal Oscillator Using Transistors.
- 14. UJT as a relaxation Oscillator.
- 15. Astable Multivibrator Using Transistors
- 16. Monostable Multivibrator Using Transistors.
- 17. Bistable Multivibrator Using Transistors
- 18. Schmitt Trigger Using Transistors.
- 19. Single Stage RC Coupled Amplifier.
- 20. Two Stage RC Coupled Amplifier
- 21. Emitter Follower.
- 22. Push Pull Complementary Symmetry Emitter Follower.
- 23. Power Amplifier Using LM380IC.
- 24. Firing Angle Control Using SCR (Half Cycle/Full Cycle).
- 25. Lamp Dimmer
- 26. Automatic Street Light Control.
- 27. Transistor Chopper.
- 28. Burglar Alarmor Fire Alarm
- 29. Power Transistor Inverter(40W/20W)
- 30. Commutation Techniques(AnyTwo)

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER III & IV

CORE PRACTICAL III

8085 MICROPROCESSOR & INTERFACING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

Course Objectives

To expose students to the operation of typical microprocessor (8085) **simulation tool**/ trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

C01	Identify relevant information to supplement to the Microprocessor.		
CO2	Set up programming strategies and select proper mnemonics and run their program on		
	the training boards/simulator.		
CO3	Practice different types of programming keeping in mind technical issues and evaluate		
	possible causes of discrepancy in practical experimental observations in comparison.		
CO4	Develop testing and experimental procedures on Microprocessor analyze their		
	operation under different cases.		
CO5	Prepare professional quality textual and computational results, incorporating accepted		
	data analysis and synthesis methods, simulation software, and word-processing tools.		

Syllabus

- 1. AdditionofTwo;8BitNumbers& SubtractionofTwo;8BitNumbers
- 2. MultiplicationofTwo;8BitNumbers

- 3. DivisionofTwo;8BitNumbers
- 4. BCDAddition
- 5. BCDSubtraction
- 6. Fill
- 7. BlockMove
- 8. 1's&2'sComplement of a 16 Bit Number.
- 9. Smallest/Largest of; N Numbers.
- 10. Ascending/Descending Order of; N Numbers.
- 11. SumofN;8-Bit Numbers.
- 12. Multi Byte Addition.
- 13. 3Digit Decimal to Two digit Hexadecimal Conversion.
- 14. Two-digit Hexadecimal to3-digit Decimal Conversion.
- 15. ADC Interface.
- 16. DAC Interface.
- 17. Waveform Generation Using DAC.
- 18. Stepper Motor Interface.
- 19. Interface with Switches & Interface with LED's
- 20. Hex Keyboard Interface.
- 21. Interface with Single Seven Segment Display.
- 22. Interfacing with Multiple Seven Segment Displays.
- 23. Interface with LCD's.
- 24. Interfacing with Solid State Relay.
- 25. Moving Display.
- 26. Blinking Display.
- 27. Digital Clock.
- 28. Traffic Light Control.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER IV

SKILL ENHANCEMENT COURSE-SEC5

NON-MAJOR ELECTIVE COURSEII

GROUP B- PAPER I

BASICELECTRONICS –II

COURSE OBJECTIVES:

To acquire the basic knowledge of digital logic levels and electronic instruments.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Examine the structure of various number systems and its application in digital design.			
CO2	Have a thorough understanding of the fundamental concepts and minimization			
	techniques used in digital electronics.			
CO3	Understand various combinational logic circuits and its applications			
CO4	Analyse and design the power supply's			
CO5	Analyze the various electronic instruments.			

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Number systems	Introduction - Binary Number System - Octal Number System - Decimal Number System - Hexadecimal number system - Conversion from one	05
		system to another - Binary Addition - Binary Subtraction - Binary Multiplication-BinaryDivision- 1's&2'sComplementSubtraction- 9's&10'sComplementSubtraction.	

II	Logic GATES	Logic GATES - NAND as a UNIVERSAL GATE - NOR as a UNIVERSAL GATE - Basis Laws of Boolean Algebra - Principle of Duality – De Morgan's Theorem.	05
III	Combination al elements	HalfAdder-FullAdder-HalfSubtractor-FullSubtractor- Encoder-Decoder-Multiplexer-Demultiplexer	05
IV	Power supplies	Half Wave Rectifier - Full Wave Rectifier - Bridge Rectifier - Capacitor Filter - Fixed IC Regulated Power Supply using 78XX - Dual IC Regulated Power Supply using 78XX & 79XX – SMPS – UPS.	05
V	Electronic instruments	Use of Multimeter - Resistance Measurement - AC & DC Voltage Measurement - AC & DC Current Measurement-TestingofDiodes&Transistors- UseofCRO-FrequencyandAmplitudeMeasurement-Use of : Strip Board, Bread board, Soldering Rod - Function Generator - Power supplies - Resistance Box - CapacitanceBox-InductanceBox	05

- 1. DigitalCircuits&Design–Salivahanan-VikasPub-IIIEdition.
- 2. ElectronicDevices&Circuits-Salivahanan-TMH-2ndEdition
- 3. PrinciplesofElectronics-V.K.Mehta-S.Chand.

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B.SC. ELECTRONICS AND COMMUNICATION SEMESTER IV SKILL ENHANCEMENT COURSE-SEC5 NON-MAJOR ELECTIVE COURSE-II GROUP B - PAPER II BIO MEDICAL ELECTRONICS –II

COURSE OBJECTIVES:

Demonstrate the devices for measurement in Intensive Care Unit, different radio diagnostic equipment and learn the principles of various electrical safety measures.

CO1	Demonstrate important equipment for measurement in Intensive Care Unit			
CO2	Demonstrate various equipment for measurement in Neonatal Intensive Care			
	Unit			
CO3	Explain medical imaging equipment and its measurements			
CO4	Explain modern radiological equipment using in modern imaging			
CO5	Demonstrate different electrical safety measures in medical places			

COURSE OUTCOMES: After completion of this course, student able to

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
I	Intensive care equipment's	Pulse Oximeter - Block Diagram & Sensor – Ventilator – Cardiac Monitor - ECG Holder - Defibrillator	05
II	Bio instruments	PaceMaker:ImplantableandExternalPacemakers- InfantWarmer-InfantIncubator-BabyPhototherapy-	05

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		Nebulizer	
III	Modern imaging systems	Ultra Sound Scanner - Color Doppler -X-RayMachine	05
IV	Medical applications	C-Arm - CT Scan – MRI Scan – Angiography - LASER in Medical Applications	05
V	Electrical safety of medical instrument s	Radiation safety - Physiological Effects Due to 50 Hertz Current Passage - Micro Shock - Macro Shock - Electrical Accidents in Hospitals - Devices to Protect Against Electrical Hazards - SMPS in Medical Equipment's.	05

REFERENCE BOOKS:

- 1. BiomedicalInstrumentation&Measurements-AnandaNatarajan-Phi-Rs.275/-
- 2. BiomedicalInstrumentationandMeasurements-LeslieCromwell-Phi-2ndEdition.
- 3. Bio-medicalInstrumentation-Dr.M.Arumugam-AnuradhaAgencies-2ndEdition.
- 4. HandbookofBiomedicalInstrumentation-R.S.Khandpur–TMH.
- 5. MedicalInstrumentation,ApplicationandDesign–JohnG.Webster-Wel-3rdEdition

- 6. A Text of Book of Medical Instruments-Ananthi-New Age International-Rs. 275/-2000 and 2000 and 20000 and 2000 and 2
- 7. Internet:AdditionalReferenceForAllUnits.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER IV

SKILL ENHANCEMENT COURSE-SEC5

NON MAJOR ELECTIVE COURSE - II

GROUP B - PAPER III

SATELLITE & CABLE TV

COURSE OBJECTIVES:

Gain knowledge about history and basics of satellites, Creates entrepreneurship opportunity the principles of various electrical safety measures.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Introduce the basics concepts of Satellites and cable networks.
CO2	Acquire knowledge about cable TV network transmission techniques
CO3	Acquire knowledge about Dish Installation
CO4	Introduction of DTH Components
CO5	Installing the DTH & supporting peripherals

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Digital Satellite System	Digital Satellite System - Block Diagram - DTH Working - DTH Antenna - DTH LNB - DTH Receiver - Additional Accessories - Complete DTH Process.	05

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II	Cable TV network	Connecting more than one TV Receiver to a Single Dish - Connecting more than one TV to a Single Satellite Receiver - Connecting more than one Dish/LNB to a Single Receiver - Changing Satellite Channels - Need for a Telephone Jack.	05		
III	Dish Installation	Dish Installation - Site Survey - Dish Roof and Wall Mounts - Adjusting the Azimuth & Elevation Settings.	05		
IV	DTH Components	Dish Antenna Connection Procedures - Precautions while installing the DTH System - Troubleshooting - Adjusting the Dish in Correct Position - LNB Testing	05		
v	Periphera ls of DTH	DD Direct Plus - Satellites Used - Comparison with Other DTH Systems - Reception of DD Direct Plus - Receiver Installation - TV/Radio Channels on DD Direct Plus.	05		

- 1. Modern DTH Digital Satellite Receiver-ManaharLotia-BPB-Rs.120/-
- 2. Modern Television Practice-Gulati-NAI-IIIEdition.
- 3. Composite Satellite & CableTV–RR Gulati–NAI.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

CORE V - ELECTRONIC COMMUNICATION SYSTEMS

COURSE OBJECTIVES:

Learn about theoretical bounds on the rates of Electronic Communication System and represent a digital signal using several modulation methods. Draw signal space diagrams compute spectra of modulated signals and apply redundancy for reliable communication.

COURSE OUTCOMES: At the end of course, student will be able to:

CO1	Understand the basics of EM, space diagram, spectrum, propagation.
CO2	Learn the generation and detection of Modulation and base band system.
CO3	Understand the generation, detection, Transmitter of FM.
CO4	Learn the AM & FM Receiver
CO5	Evaluate the performance of PCM, DPCM and DM in a digital communication
	system

Syllabus

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification	Instruction
		attributes K1, K2, K3, K4)	
		Introduction to EM waves - Reflection and	
		refraction of radio waves at the surface of the	
	Propagation of radio waves	earth – Ground wave propagation-Sky wave	
Ι		propagation – Space wave propagation –	10
		Structure of the Atmosphere – Critical	
		frequency - Skip distance – Maximum Usable	
		frequency (MUF) – Virtual height.	
II	AM generation & transmission	Need for modulation – Amplitude modulation	10

		- Frequency Spectrum of the AM Wave -	
		Modulation Index – Power relations in the AM	
		Wave – AM generation – AM Transmitter	
		Forms of Amplitude Modulation – Evolution	
		of SSB – Balanced Modulator – Methods of	
		SSB Generation – Vestigial side band	
		Transmission.	
		Frequency Modulation - Frequency Spectrum	
		of the FM Wave – Modulation Index – Effect	
TTT	FM generation	of Noise – Adjacent & Co-Channel	10
III	& transmission	Interference – Wide Band & Narrow Band	10
		FM-FM Generation – Direct and Indirect	
		methods - FM Transmitter – Pre-Emphasis.	
		AM Receiver - TRF Receiver - Super	
		Heterodyne Receiver – Image Frequency	
	AM & FM	Rejection – Frequency Changing & Tracking	
		– Choice of IF – AM Detection – AGC – SSB	
IV	reception	Detection. FM Receiver – Amplitude Limiter	10
	-	– De-Emphasis – FM Detection – Balanced	
		Slope Detector – Phase Discriminator – Ratio	
		Detector.	
		PAM Modulation & Detection – PWM	
		Modulation & Detection - PPM Modulation &	
	Pulse	Detection - Sampling Theorem – Quantization	
\mathbf{V}	modulation	& Quantization Error – PCM Modulation &	10
		Detection - Companding - ASK – FSK –	
		BPSK – QPSK – DPSK	

- 1. ElectronicCommunicationSystems-Kennedy-TMH–IVEd
- 2. ElectronicCommunicationSystems-Roddy&Collen-PHI-IVEd
- 3. ElectronicCommunications–SanjeevGupta–KhannaPublications.
- 4. PrinciplesofCommunicationEngineering-AnokhSingh-S.Chanda

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

CORE VI - IC'S AND THEIR APPLICATIONS

COURSE OBJECTIVES:

This course elaborates the production methods of IC's and various application of them including logic, OP-AMPs.

COURSE OUTCOMES: After completion of this course, student able to

CO1	
	Understand the method of IC's fabrication Techniques.
CO2	Studythe various circuits of Logic operations using ICs.
CO3	Get the knowledge of various OP-Amp circuits
CO4	Study the principle of Filter.
CO5	Study the principle of Timer& Phase locked loop circuits.

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	IC fabrication process	Introduction - Basic Planar Process - Fabrication of a Typical Circuit - Active and Passive Components - Fabrication of FET, MOSFET & CMOS.	10
II	Logic Family's	CharacteristicsofIC's-DiodeLogic-TransistorLogic- RTL-DCTL-IIL-DTL-HTL-TTL-CMOS–ECL- ComparisonofLogicFamilies.	10

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III	OP – AMP ' S	Introduction - The ideal OP-AMP - OP-Amp Stages - OP-Amp Parameters - Inverting &NonInverting Amplifier - Adder - Subtractor - Multiplier - Divider - Integrator - Differentiator - V to I Converter - I to V Converter.	10
IV	Filters	Low Pass Filter – High Pass Filter – Band Pass Filter – Band Reject Filter - Solving of Simultaneous Equations – Solving of Differential Equations.	10
V	555 Timer & 565 PLL	555: Functional Diagram -AstableOperation - Monostable Operation - Linear Ramp Generator. PLL: Basic Principle – 565 PLL - Frequency Translation - Frequency Multiplier - Frequency Divider - AM Detection – FM Detection.	10

- 1) LinearIC's-RoyChoudhury-NAI-4thEdition.(UNITI)
- 2) ElectronicCircuits–Salivahanan–TMH-IIEdition(UNITII)
- 3) OP-Amps-Gayakwad-PHI-4thEdition-(UNITIII,IV&V)

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

CORE VII – ELECTRONIC INSTRUMENTATION

COURSE OBJECTIVES:

- To introduce the fundamental electronic instrumentation things.
- To expose the students to upgrade their knowledge in industry side.
- To get familiarize with recent instrumentation technologies.

COURSE OUTCOMES: After completion of this course, student able to

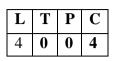
CO1	Gain knowledge in the fundamental principle of electromechanical instruments.
CO2	Become familiar with Bridges Network circuits
CO3	Acquire the knowledge in different types of oscilloscopes.
CO4	Understand the signal generation techniques.
CO5	Understand the working function and application of transducer.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
	Electro	DC Ammeter - DC Voltmeter - Voltmeter Sensitivity -	
Ŧ	mechanical	AC Voltmeter - Considerations in Analog Voltmeter -	05
1	indicating	Series & Shunt Type Ohmmeter - Calibration of DC	05
	instruments	Instruments – Study of a Typical Digital Multimeter.	

		Wheatstone Bridge - Balance Equation of General	
II	D ' I	ACBridges - Capacitance & Inductance Comparison	05
	Bridges	Bridge - Maxwell – Hay - Schering - Wien - Kelvin &	05
		Kelvin's Double Bridge .	
		Block diagram - CRT - Vertical Deflection System -	
		Delay line - Horizontal Deflection System - CRT	
III	Oscilloscope	screens & Graticules - Oscilloscope Probes -	05
		Measurement of Frequency, Amplitude & Phase -	
		Lissajou's Patterns. Protocols.	
	Signal	Sample & Hold Circuit - Instrumentation Amplifier -	
IV	generation &	Function Generator - Pulse Generator - Q Meter -	05
	signal	Vector Impedance Meter - Wave Analyzer - Harmonic	05
	analysis	Distortion Analyzer.	
		Resistive Transducers - Inductive Transducers -	
V	Tuonaduoona	Capacitive Transducers - Piezo Electric Transducer -	05
	Transducers	Thermo Electric Transducers – Temperature	05
		Transducers – Microphones & Loud Speakers.	

- 1. ElectronicInstrumentation-H.S.Kalsi-TMH.
- 2. ModernElectronicInstrumentation&MeasurementTechniques-Cooper -PHI.
- 3. ElectronicMeasurements&Instrumentation–Salivahanan–S.Chand -Rs.270/-



B.SC. ELECTRONICS AND COMMUNICATION SEMESTER V CORE VIII - BIOMEDICAL INSTRUMENTS

COURSE OBJECTIVES:

To introduce a fundamental of Human Physiology and explore the human body parameter measurements setup's. A small project can be done by the students by the end of the semester.

COURSE OUTCOMES: At the end of course, student will be able to :

CO1	Understand the human physiology of biomedical system
CO2	Measure biomedical and physiological information
CO3	Discuss the application of Electronics in diagnostics and therapeutic area.
CO4	Make the students understand the concepts Pulse Oximeter and pace
04	maker techniques
CO5	Give basic ideas about modern medical imaging application

Syllabus

Unit	Unit Title	Intended learning Chapters (Program specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Introduction to Human Physiology's	Introduction to Human Physiology – Micro Electrodes – Skin Surface Electrodes – Needle Electrodes – Reference Electrodes - Digital Thermometer – Sphygmomanometer - Electronic Stethoscope	10
II	Recorders and meters	ECG - EEG - EMG - – Cardiotocography - Electro Oculography - Electro Retinography - Poly Somnography - Spirometer - Blood Flow Meter - Doppler - Audiometer	10
III	Ts	TS. Upper Endoscope - Lower Endoscope -	10

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		ENT Endoscope Diathermy - Surgical Diathermy- Micro WaveDiathermy- MultiparaPatientMonitor.	
IV	Intensive carets	Pulse Oximeter - Block Diagram & Sensor – Ventilator – Cardiac Monitor - ECG Holder - Defibrillator - Pacemaker:ImplantableandExternalPacemakers -InfantWarmer-InfantIncubator- BabyPhototherapy– Nebulizer.	10
V	Electrical safety of medical instruments	Radiation Safety - Physiological Effects Due to 50 Hertz Current Passage - Micro Shock - Macro Shock - Electrical Accidents in Hospitals - Devices to Protect Against Electrical Hazards – SMPS in Medical Equipments.	10

- 1. BiomedicalInstrumentation&Measurements-AnandaNatarajan-PHIRS.275/-
- 2. BiomedicalInstrumentationAndMeasurements-LeslieCromwell-PHI-2ndEdition.
- 3. Bio-Medical Instrumention -Dr.M.Arumugam- AnuradhaAgencies-2ndEdition
- 4. HandbookOfBiomedicalInstrumentation- R.S.Khandpur–TMH.
- 5. MedicalInstrumention,ApplicationAndDesign–JohnG.Webster-WEL-3rdEdition
- 6. ATextOfBookOfMedicalInstruments-Ananthi-NewAgeInternationaRs.275/-
- 7. Internet:AdditionalReferenceforallUnits.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

ELECTIVE I - PAPER I-Group A 8051 MICROCONTROLLER AND INTERFACING

COURSE OBJECTIVES:

To understand the basics of micro controller architecture and memory systems. learn the assembly language programming instructions and writing programs & to exploit the abilities for the designand peripheral interfacing with real time domains.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the evolution of microcontroller8051 architectures, pin functions and
	bus timing.
CO2	Learn the instruction format, and to create the assembly language program with
	looping techniques.
CO3	Investigate the interfacing techniques for keyboard and optical devices.
CO4	Compute and design for converter and real time applications.
CO5	Design and development of memories.

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification attributes K1, K2, K3, K4)	Instruction
		, ,	
Ι	8051	Features - Signals - Architecture - RAM Structure -	10
	Microcontrol ler	SFR's - Power Saving Modes - Interrupt logic - Timer	
		Logic - Serial Logic	

PE	ERIYAR UNIV	ERSITY	
II	Instruction set	Datatransfer-Arithmetic–Logical–Bitmanipulation– BranchingInstructions–Stack&StackOperations– Addressing Modes- Simple Programs.	10
III	Interfacin g with IO	Interfacing With : Switches & Matrix Keyboard - LED'S - Single & Multiple Seven Segment Displays – LCD.	10
IV	Interfacin g with Application	InterfacingWith:ADC0809IC–DAC0800IC- StepperMotor–DCMotor–TrafficLightControlSystem.	10
V	Memories	ROM – PROM – EPROM(2764) - EEPROM – NVRAM - Static RWM (6264)- DynamicRWM(TC511000)-RWMRefreshing– PseudostaticRWM.	10

- 1. The 8051 Microcontroller Embedded Systems Mazidi&Mazidi Pearson 2nd Edition.
- 2. Introduction to Microprocessor -AP Mathur TMH –3rdEdition(Unit-V)

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

ELECTIVE I - PAPER II-Group A PIC16F877 MICROCONTROLLER AND INTERFACING

COURSE OBJECTIVES:

- 1. Through this course, the students will be exposed to hardware details of PIC16F877 MICROCONTROLLER with the related signals and their implications.
- 2. They will also learn programming and interfacing of PIC16F877.
- 3. They will also be aware of the interfacing and its application.
- 4. Lastly the students will have a basic idea on memory.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the architecture of PIC16F877 MICROCONTROLLER.
CO2	Impart the knowledge about the instruction set.
CO3	Interface LCD, Keyboard, ADC, DAC, Sensors, Relays, DC motor and Stepper motor with PIC16F877 microcontroller.
CO4	Understand the basic idea about Memories and its applications.

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification attributes K1, K2, K3, K4)	Instruction
Ι	PIC16f877 Microcontroller	Features – Signals - Architecture – Memory Organization – Watch Dog Timer – Reset Types – Oscillator Types – Power Down Modes – I/O Ports – CCP Module – SSP Module. (USART, SPI, I2C & ICSP)	10

PI	ERIYAR UNIVERS	ITY	
II	Instruction set	ByteOriented–BitOriented- Literal&ControlInstructions– Stack&StackOperations-AddressingModes– Simple Programs - Timer logic – interrupt logic – Serial Logic – ADC.	10
III	Interfacing with I/O	Interfacing With: Switches & Matrix Keyboard - LED'S - Single & Multiple Seven Segment Displays – LCD.	10
IV	Interfacing with Application	InterfacingWith:ADC0809IC–DAC0800IC- StepperMotor–DCMotor– TrafficLightControlSystem.	10
V	Memory	ROM – PROM – EPROM(2764) - EEPROM – NVRAM - Static RWM (6264)- DynamicRWM(TC511000)-RWMRefreshing– PseudostaticRWM.	10

- 1. PIC16F877DataBook–MICROCHIP.
- 2. Fundamentals of Microcontrollers and Applications in Embedded Systems (With the PIC18 MicrocontrollerFamily)–RameshSGaonkar–PRI–RS.300/-
- 3. DesignWithPicMicrocontrollers–JohnB.peatman–Pearson
- 4. Introduction to Microprocessors-APM athur-TMH-3rdEdition (UNIT-V).

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V

ELECTIVE I - PAPER III - Group A

PROGRAMMABLE LOGIC CONTROLLERS

COURSE OBJECTIVES:

To provide knowledge levels needed for PLC programming and operating, PLC functions, Data Handling processes & make the students how devices to which PLC input and output modules are connected.

COURSE OUTCOMES: After completion of this course, student able to

C01	Ability to gain knowledge on Programmable Logic Controller.
CO2	Create ladder diagrams from process control function
CO3	Understand the knowledge about varies type of PLC registers
CO4	Develop the PLC application program
CO5	Design the different type of PLC functions, Data Handling Function.

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	PLC Basics	Advantages and Disadvantages – Overall PLC System – Input & Output Modules – Printing PLC Information – CPU – Memory – Processor – I/O Modules – Power Supplies – Programming Equipment – ProgrammingFormats– ConstructionofPLCLadderDiagram– ProcessorsScanningConsiderations–PLC OperationalFaults–InputON/OFFSwitchingDevices– InputAnalogDevices–OutputON/OFFDevices– Output Analog Devices.	10

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II	PLC Programming	Input Instructions – Outputs: Coils, Indicators & Others – Operational Procedures – Contact and Coil I/O ProgrammingExamples(Any3)–	10
		DigitalLogicGates-	
		BooleanAlgebraPLCProgramming–Conversion	
		Examples(Any3)–	
		LadderDiagramsandSequenceListings-	
		LargeProcessDiagramConstruction.	1.0
III	PLC Functions	GeneralCharacteristicsofRegisters– ModuleAddressing–HoldingRegisters–	10
		InputRegisters:Single and Group – Output Registers:	
		Single and Group – PLC Timer Functions –	
		Examples of Timer Function Industrial Applications	
		(Any 3) – PLC Counters – Examples of Counter	
		Function Industrial Applications (Any3).	
IV	Intermediate		10
	functions	PLC Addition and Subtraction – PLC Repetitive	
		Clock – PLC Multiplication, Division, Square Root,	
		Trigno metric and Log Functions – Other Arithmetic	
		Functions – Basic Comparison Functions – Basic	
		Comparison Function Applications (Any 3).	10
V	Data handling functions	Skip Function and Applications – MASTER CONTROL RELAY Function and Applications –	10
		Jump with NonReturn–JumpwithReturn–	
		MOVEFunction&Applications–	
		MovingLargeBlocksofPLCData– PLC Table and	
		Register Moves – PLC FIFO Function – FAL – ONS	
		– CLR and SWEEP Functions – Bit Patterns in a	
		Register – Changing a Register Bit Status – Shift	
		Register Functions and Applications. PLC	
		Networking.	
		č	

TEXT BOOK

Programmable Logic Controllers - John W. Webb & Ronald A. Reis - PHI - V Edition - Rs.295/-

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI

CORE IX - PC HARDWARE NETWORKING & TROUBLESHOOTING

COURSE OBJECTIVES:

This course guides the complete view of hardware of the personal computer and possible troubleshooting.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the motherboard types and connection with various types of RAM
CO2	Study the various versions of BIOS and their functions.
CO3	Get the knowledge of Keyboard and Mouse connections and their
	troubleshooting
CO4	Understand the basic of Hard disk and their connections, with CD and DVD
	storage devices& the printer types
CO5	Study the networking and their connections with assembling of PC in the cabinet.

Syllabus

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Mother board	Components - Support Circuits – Connectors - Installation - Troubleshooting – BIOS Beep Codes - Study of a Latest Motherboard - Form Factor - PC Assembly - Cabinet Form Factor .	10

п	Memory systems	MemoryModules-CacheMemory-ShadowMemory- CommonMemoryErrors-Battery-BIOSFunctions- BIOSSetup–BIOSUpgrade.	10
ш	Input device Troubleshoo ting	KEYBOARD:Organization - Troubleshooting - Ergonomics MOUSE: Connection-Resolution-Installation- Troubleshooting.	10
IV	Output device Troubleshoo ting	 HARDDISK: Form Factor-Storage Capacity-Disk Geometry-Interfacing–Installation, Formatting &Troubleshooting. PRINTER: Types, Interface & Troubleshooting. 	10
v	Networking	NETWORKING: Setting up a Network–Preparation for Network Installation–Network Configuration – Sharing Computer–Sharing Printer–Troubleshooting Networking. VIRUS: Types-Working-Symptoms-Antivirus.	10

- 1. ModernComputerHardwareCourse-ManoharLotia-BPB-Rs.360/-
- 2. IBMPcandClones–Govindarajulu–Tmh
- 3. IBMPCAdvancedTroubleShootingAndRepairingGuide–Robert.c.brenner-Phi.
- 4. TroubleShooting,Maintenance&RepairingPc's-stephenJ.bigelow-Tmh-IiEd
- 5. UpgradingandRepairingPC's-ScottMueller–Pearson-18th-Edition.
- 6. DOS6&6.22AnIntroduction-ManoharLotia-BPB-Rs.99/-
- 7. DOS6&6.22Companion-SatishJain-BPB-RS.210/-

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B.SC. ELECTRONICS AND COMMUNICATION SEMESTER VI

CORE X - NETWORK COMMUNICATION & SECURITY

COURSE OBJECTIVES:

- Describe various communications networks and their main component
- Identify the advantages and disadvantages of a network.
- Identify the function of a firewall, and how it keeps a computer secure and safe from viruses. Prepare a plan for anti-virus protection.

COURSE OUTCOMES: At the end of course, student will be able to :

CO1	Identify the components associated with Transmission methods.	
CO2	Students will details of s network architecture, Topology technology etc	
CO3	Understand networks protocols and network management.	
CO4	The concept of encapsulation and its relationship to layering in the network model.	
CO5	An ability to understand and analyze the issues in providing Quality-Of- Service for network multimedia applications such as Internet, telephony& network security	

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification attributes K1, K2, K3, K4)	Instruction
I	Transmission methods	Digital Signal Analog Transmission – Baud Rate - Analog Signal Digital Transmission – Parallel & Serial Communication – Asynchronous & Synchronous Communication – Simplex – Half Duplex - Full Duplex – Multiplexing - De- multiplexing - Types of Multiplexing.	10
II	Network	Mesh Topology – Star Topology – Tree	10

PERIYAR UNIVERSITY				
	topologies	Topology - Ring - Bus - Hybrid - Basics		
		of Switching – Router & Routing – Internet		
		Topology – Architecture of an ISP –		
		Logical Types of Topology.		
		OSI Model – Physical Layer – Data Link		
		Layer – Network Layer – Transport Layer –		
III	Network	Session Layer – Presentation Layer –	10	
	protocols	Application Layer – Overview of Network		
		Protocols.		
	LAN technologies	Introduction – LAN Hardware –		
		Implementing LAN – Fast LANS -		
IV		Nonstandard LANS – Extending LANS –	10	
		Virtual LANS – Token Passing Networks –		
		FDDI – MAN – WAN.		
		Introduction – Dial up Access – Leased		
		lines – DSL - Cable Modems – DTE – DCE		
	Internet access	Interface - RS-232 & RS-449 Interface -		
V	& network	SONET.	10	
	security	NETWORK SECURITY: Introduction -		
		Types of Computer Attacks – Firewall –		
		Virtual Private Network-Cryptography.		

- 1. DataCommunication&Networks-Achyut.S.Godbole&AtulKahate–TMH– 2ED(Units:I,II,III&V)
- 2. Advanced Computer Networking(ConceptsandApplications)-SatishJain–BPB–Rs.195/-(Unit:IV&V)
- 3. Data Communication And Networking(UPDATEDEDITION)–SatishJain– BPBPublications.Rs.270/-
- 4. ComputerNetworks–UYLESSBLACK–PHI-IINDEDITION.
- 5. ComputerNetworks–ANDREW.S.TANENBAUM–PHI.
- 6. Communication Protocol Engineering-Pallapa Venkataramand S.S.Manvi-PHI.
- 7. Networking Concepts And Netware–Anand–Himalaya Publications

B.SC. ELECTRONICS AND COMMUNICATION

L T P C 5 3

SEMESTER VI

ELECTIVE II - PAPER I-Group B

MODERN TELEVISION SYSTEMS

COURSE OBJECTIVES:

To introduce the basics of picture transmission and reception, analysis and synthesis of composite video signal, receiver and picture tubes and television camera tubes also study various colour television systems.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the fundamental concepts of television standards.		
CO2	Acquire the knowledge in basic principle of receiver circuits.		
CO3	Distinguish the principle of vertical and horizontal sync separation.		
CO4	Understand different colour television systems used worldwide and its compatibility.		
CO5	Evaluate the detailed operation of camera and picture tube of monochrome and colour television.		

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Television Standards	Aspect Ratio - Scanning – Number of Scanning Lines - Interlaced Scanning - Vertical Resolution and Horizontal Resolution – Horizontal & Vertical Sync	10
		Details - Composite Video Signal - Channel Bandwidth – Vestigial Side Band Transmission &	
		Reception – Complete Channel Bandwidth – FM Channel Bandwidth – Allocation of Frequency Bands	

	<u>KIIAK UNIVE.</u>	For TV Transmission - Positive and Negative	
		Modulation – CCIR–B Standards.	
II	Receiver	RF Tuner –Tuner Types - Various Sections of a VHF	10
	Circuits	Tuner – UHF Tuner – Electronic Tuning – Video IF	
		Section – IF Amplifier – VSB Correction - Video IF	
		Amp using IC CA 3068 - Video Detector Operation &	
		Requirements – Video Amplifier Operation &	
		Requirements - Coupling Methods - Video Amp	
		Using IC TBA 890.	
III	Sync Separator	Block Diagram – Vertical & Horizontal Sync	10
		Separation - Vertical O/P Stage & Requirements -	
		Vertical O/P Stage IC's – EHT generation - S	
		Correction – Line O/P Stage Using Transistors & IC	
		CA 920 - AGC - Types of AGC - Sound Section -	
		Sound Take Off Circuit – Inter Carrier Sound IF Amp	
		- AM Limiting - FM Detection - Any One FM	
		Detector - Sound Section IC CA 3065.	
IV	Colour	Compatibility - Natural light - Colour perception -	10
	Television	Three colour theory – Chromaticity diagram –	
		Luminance, Hue and Saturation – Luminance & Colour	
		difference signals - Frequency interleaving -	
		Bandwidth for Colour Signal Transmission -	
		Modulation of Colour Difference Signals - Colour TV	
		Standards.	
V	Television Transmission & Reception	Monochrome TV Camera Tubes (Any One) -	10
		Monochrome Picture Tube - Block diagram of	
	_	Monochrome TV Transmitter and Receiver - Colour	
		TV Camera – Colour Picture Tubes (Any One) – PAL-	

	D Coder – PAL Decoder – Merits & Demerits - Low				
	Voltage Power Supply – High Voltage Power Supply –				
	SMPS - Merits & Demerits.				

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- $1.\,MonochromeAndColourTelevision-Gulathi-Nai-IiEdition$
- 2. ColourTelevisionPrinciplesAndPractice-Gulathi-Nai
- 3. ModernTelevisionPractice-Gulathi-Nai-IIIEdition

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI-ELECTIVE II - PAPER II-Group B

RADAR AND NAVIGATIONAL SYSTEMS

COURSE OBJECTIVES:

To introduce the fundamental concepts of RADAR and navigational aids.

Expose the students to different types of RADAR systems and Navigation, get familiarize with recent technologies in methods of navigation.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Gain knowledge in the fundamental principle of RADAR.
CO2	Become familiar with fundamentals of different types of RADAR.
CO3	Gain in-depth knowledge about the different types of RADAR transmitter and
	receiver and their operations.
CO4	Understand the signal detection in RADAR and various detection techniques.
CO5	Understand the navigational aids and modern navigation methods.

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2,	Hours of Instruction
		K3, K4)	
Ι	Introduction	Basic Radar – The simple form of the Radar Equation -	10
	to Radar	Radar Block Diagram - Radar Frequencies –	
		Applications of Radar – The Radar Equation -	
		Transmitter Power - Pulse Repetition Frequency -	
		Antenna Parameters - Other Radar Equation	
		Considerations.	
II	MTI and	Doppler and MTI Radar- Moving Target Detector -	10
	Pulse Doppler	MTI from a Moving Platform - Pulse Doppler Radar –	
	Radar		

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		Other Doppler Radar Topics - Tracking with Radar –	
		Automatic Tracking with Surveillance Radars.	
III	RADAR	Linear Beam Power Tubes - Solid State RF Power	10
	Transmitter and Receiver	Sources - Magnetron - Crossed Field Amplifiers - Other	
		RF Power Sources - Other aspects of Radar Transmitter	
		- The Radar Receiver - Superheterodyne Receiver -	
		Duplexers and Receiver Protectors- Radar Displays	
		Propagation Radar Waves - Atmospheric Refraction -	
		Standard propagation - Nonstandard Propagation - The	
		Radar Antenna - Reflector Antennas .	
IV	RADIO	An Aural Null Direction Finder - The Goniometer -	10
	DIRECTIO N FINDING	Automatic Direction Finders - The Commutated Aerial	
	& RANGES	Direction Finder - The LF/MF Four course Radio	
		Range - VHF Omni Directional Range - VOR	
		Receiving Equipment - Loran-A Equipment - The	
		Decca Navigation System - Decca Receivers - The	
		Omega System.	
V	Methods of	Operation of DME - TACAN Equipment Instrument	10
	Navigation	Landing System - Ground Controlled Approach System	
		- Microwave Landing System - Navigation Over the	
		Earth - Components of an Inertial Navigation System.	

REFERENCE BOOKS :

- 1. IntroductionToRadarSystems-Skolnik-Tmh-IiiEdition2003
- 2. RadarPrinciples-PeytonZPeebles-JohnWiley-2004
- 3. PrinciplesOfRadar-JcToomay-PhiliEdition-2004
- 4. Microwave&RadarEngineering–Kulkarni–UmeshPublications
- 5. RadarSystem&RadarAidsToNavigation–Sen&Battachariya–KhannaPublications.

B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI

ELECTIVE II - PAPER III Group B

SATELLITE, CABLE AND DTH SYSTEMS

COURSE OBJECTIVES:

- To introduce the basics concepts of Satellites and cable networks.
- To make the students assemble and troubleshoot DTH there self.
- Creates entrepreneurship opportunity.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Gain knowledge about history and basics of satellites
CO2	Acquire knowledge about cable TV network transmission techniques
CO3	Acquire knowledge about digital satellite TV network transmission techniques
CO4	Introduction of DTH Components
CO5	Installing the DTH & supporting peripherals

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Satellite systems	Geo- Stationary Satellite – Satellite Communication System – Satellite Electronics – International and Regional Direct Broadcasting Satellites – Indian Domestic Satellites – Domestic Broadcasting Systems.	10
II	Cable tv systems	Cable Signal Sources – Cable Signal Processing – Cable Signal Distribution – Bidirectional Networks –	10

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		Scrambling of TV Signals – Cable Signal Convertors.	
		Scramoning of TV Signals – Cable Signal Convertors.	
III	Digital satellite tv	Digital Satellite Transmission – Digital Satellite Reception and Decoding – DTH TV – Digital TV Receiver – Merits of Digital TV Receiver – DTT.	10
IV	DTH working	DTH Antenna – DTH LNB – DTH Receiver – Additional Accessories – Complete DTH Process – Connecting more than One TV Receiver to a Single Dish , Connecting more than one tv to a Single Satellite Receiver – Connecting more than one Dish/LNB to a Single Receiver – Changing Satellite Channels – Need of Telephone Jack.	10
V	Dish installation	Site Survey – Dish Roof and Wall mounts – Adjusting the Azimuth and Elevation Settings – Dish Antenna Connection Procedures – Precautions - Trouble Shooting – Adjustment to Correct Position – LNB Testing – DD Direct Plus – Satellites Used – Comparison with Other DTH Systems – Reception of DD Direct Plus – Receiver Installation – TV/Radio Channels on DD Direct Plus.	10

REFERENCE BOOKS :

- 1. ModernTVPractice–R.R.Gulati–NAI–IIIrdEdition
- 2. ModernDTHDigitalSatelliteReceiver-ManahorLotia-BPB-Rs.120/-

- 3. CompositeSatelliteandCableTV-R.R.Gulati-NAI.
- 4. SatelliteCommunication-DennisRoddy–TMH

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI

ELECTIVE II - PAPER IV- Group B

MOBILE COMMUNICATON & SERVICING

COURSE OBJECTIVES:

- To introduce the basics of Cellular structure and operation.
- It provides entrepreneurship opportunity Practical oriented.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the fundamental concepts of Cellular.
CO2	Acquire the knowledge in basic principle of BTS transmission.
CO3	Understand the different accessing technology.
CO4	Understand the chip level software servicing of mobile
CO5	Understand the servicing of mobile

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification attributes K1, K2, K3, K4)	Instruction
Ι	Mobile data communicati on	Introduction – Cellular Radio – Elements of a Cellular Network – CellularTelephony – Radio Propagation – Speech Coding – Error Coding and ErrorCorrrection.	10
II	Mobility Management	Mobility Management - Hand Off Management – Hard Hand Off – Soft Hand Off – Switching and Authentication – MTSO Interconnections- Circuit Switched and Packet Switched Data Services on Cellular Networks.	10
III	Multiple access technologies	Introduction - Frequency division multiple access - Time division multiple	10

		access - Code Division Multiple Access – Spread Spectrum Techniques.	
IV	Mobile servicing	Hardware/Software Repairing - Various Locks - Installation of : UFS Driver, UFS Suite & Flashing Files - IMEI Number Detection – Mobile Utility Codes.	10
v	Other mobile service tools	Ultrasonic Cleaner - Computer Connectors - SIM Card Reader - Memory Card Reader - Mobile Virus - Virus Prevention - Removing Virus - Health Hazards with Mobiles - SAR.	10

REFERENCE BOOKS

- 1. WirelessCommunicationsAndNetworking–MadeSimple–SatishJain– BPBPublications.Rs.135/- (Units:I,II&III)
- Modern Mobile Phone Repair using Computer Software & Service Devices -ManaharLotia - BPB- 120/-(UnitsIV&V)
- 3. ModernMobilePhoneUnlocking&UtilityCodesForGSM&CDMAPhones-ManaharLotia-BPB- Rs.99/-(UnitIV)
- 4. MobileCellularTelecommunication-IIEdition-William CYLee-TMH
- 5. MobileCommunications–Schiller–Pearson–IIEdition.
- 6. WirelessCommunications–Stalling–PearsonIIEdition.
- 7. Mobile&PersonalCommunicationSystems&Services-RajPandya-PHI-Rs.250/-

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B.SC. ELECTRONICS AND COMMUNICATION SEMESTER VI ELECTIVE III - PAPER I GROUP C PCB DESIGN AND FABRICATION

COURSE OBJECTIVES:

Understand the need for PCB and steps involved in PCB Design and Fabrication process.PCB (Printed Circuit Board) designing is an integral part of each electronics products and this program is designed to make students capable to design their own projects PCB up to industrial grade.

COURSE OUTCOMES: At the end of course, student will be able to :

CO1	Appreciate the necessity and evolution of PCB, types and classes of PCB.
CO2	Understand the steps involved in schematic, layout, fabrication and assembly process of PCB design.
CO3	Familiarize Schematic and layout design flow using CAM Tools.
CO4	This is a basic course for designing of PCB using software. Design (schematic and layout) and fabricate PCB for simple circuits.
CO5	Understand the environmental concerns

Syllabus

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification	Instruction
		attributes K1, K2, K3, K4)	
		PCB Introduction – Surface Mount and	
Ι	Quick start &	Through hole – Prototyping – Installing	10
	components	EAGLE Light Edition on Windows -	10
		Light Edition Limitations – Creating	

		New Project – Drawing the Schematic –	
		Electrical Rule Check – Laying out the	
		Board – U.S. Versus European Circuit	
		Symbols – Resistors – Capacitors –	
		Transistors & Diodes – IC'S– Connectors	
		– Other Components – Buying	
		Components – Paper PCB.	
		The Anatomy of the Schematic Editor –	
		The Command Toolbar – Nets – Buses –	
		Worked Examples.	
	Editing	PCB LAYOUT:	
II	schematics&	Experimenting – Layers – The	10
	PCB layout	Command Toolbar - The Grid -	
		Sound Meter Layout (Through Hole)	
		– Sound Meter Layout (Surface	
		Mount) – Manual Layout.	
		Gerber Files – Loading a CAM Job –	
		Running a CAM Job – Measure Twice,	
		Cut Once – Submitting a job to a PCB	
		Service – Instructions – Photo etching –	
	PCB fabrication	Milling PCBs – Toner Transfer.	
III	&PCB soldering	PCB SOLDERING: General Tools -	10
	ar ch soluting	Tools for Surface Mount Devices –	
		Soldering Through Hole PCB's – SMD	
		Hand Soldering – SMT with Hot Air	
		Gun – Using a Reflow Oven.	
		Commands - Scripts – User Language	
		Programs – Creating Library – Copying	
IV	Commands &	a Device from Another Library – The	10
T A	scripts	Part Editor – Devices, Symbols, and	±.
	scripts	-	
	scripts	Packages – Editing a Part – Creating	
	scripts	-	
v	Environmental	Packages – Editing a Part – Creating	10

Recovery Techniques – Air Pollution –	
Recycling of PCB's – Environmental	
Standards – Safety Precautions – Toxic	
Chemicals.	

REFERENCE BOOKS :

- 1. MakeYourOwnPCB'sWithEAGLE–SimonMonk–McgrawHill(2014)
- 2. EAGLEMANUAL-VersionV-SeventhEdition.
- 3. PCBDesignFabrication,AssemblyandTesting–Dr.R.S.Khandpur–TMH.

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4. PCBDesign&Fabrication–Walter.C.Bosshart–TMH

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI

ELECTIVE III - PAPER II - GROUP C

PROGRAMMING USING VERILOG HDL

COURSE OBJECTIVES: Students will try to learn:

Understand the concepts of Verilog Language, inspect how effectively ICs are embedded in package and assembled in PCBs for different application. Design and diagnosis of processors and I/O controllers used in VHDL.

0001	
CO1	Describe the basic language features of Verilog HDL and the role of HDL in
	digital logic design.
CO2	Describe the basic concepts Verilog HDL
CO3	Describe Verilog model for sequential circuits and test pattern generation.
CO4	Design data flow modeling.
CO5	Synthesize different types of behavioral modeling.

COURSE OUTCOMES: At the end of course, student will be able to :

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification	Instruction
		attributes K1, K2, K3, K4)	
		Overview – Design Flow – Hierarchical	
T	Basic Verilog	Modeling Concepts – Design	10
L	topics	Methodologies - Modules - Instances -	10
		Components of Simulation.	
тт	Lexical Conventions - White Space -	10	
II	Basic concepts	Comments – Operators – Number	10

		Specification – Strings – Identifier and	
		Keywords – Data Types–Nets–Registers –	
		Vectors Arrays - Memories - Parameters-	
		Strings–System Tasks and Compiler	
		Directives.	
		Modules – Ports – Port Declaration – Port	
TTT	Modules and	Connection Rules - Connecting Ports to	10
III	ports	External Signals – Gate Level Modelling –	10
		Gate Types.	
		Continuous Assignment – Delays –	
	Data flow modeling	Expression – Operators and Operands –	
IV		Operator Types - BehaviourModelling -	10
		Structure Procedures – Procedural	
		Assignments – Timing Controls.	
		Conditional statements – Multiway	
		Branching – Loops – Sequential and	
v	Behavioral	Parallel Blocks – Generate Blocks – Logic	10
v	modeling	Synthesis with Verilog HDL - Impact of	10
		Logic Synthesis – Verilog HDL Synthesis –	
		Synthesis Design Flow.	

Text Book :

1. Verilog HDL – A Guide to Digital Design and Synthesis – Samir Palnitkar – Pearson - II EDITION.

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER VI

ELECTIVE III - PAPER III - GROUP C

ELECTRONIC DEFENSE SYSTEMS

COURSE OBJECTIVES: Students will try to learn:

Understand the concept of electronic warfare. and acquainted with the basic characteristics and requirements of electronic warfare receivers, the basic principles of electronic countermeasures, its types and classes of ECM and the parameters of radars, and radar systems pertaining to ECM,

COURSE OUTCOMES: At the end of course, student will be able to :

CO1	Gain	knowledge	in	the	fundamental	principle	of	Electronic
	defenc	esystem,weap	on sy	stems				
CO2	Analyz	ze the functio	ning	and ir	nterrelations of	subsystems	in ar	electronic
002	warfar	e system						
CO3	Develo	op technical ar	chite	cture c	of electronic inte	ercept syster	ns in	preliminary
000	system	design level						
CO4	Develo	op basic simu	ation	and a	analysis tools fo	or the assess	sment	of a given
	ECM.							

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification	Instruction
		attributes K1, K2, K3, K4)	
		Introduction – Systems in Use in the	
	Electronic defense	Armed Forces – The Main Weapon	
		Systems – Objectives & Organization	
Ŧ		of Electronic Defence – Operational	10
I		Objectives - Information Operation	10
		&Warfare – Need for the Study of	
		Weapon Systems.	

II	Weapon systems	Artillery Systems – Missile Systems (Any 3) – Passive Antiradiation Missiles – Stealth Aircraft – Communication Systems – Information Operations.	10
III	Electronic intercept systems	Introduction – Radar Warning Receivers – Electronic Support Measures – Omnidirectional Antennas – Antennas for Direction Finding – Digital Receivers – Electronic Intelligent Systems – Advanced Passive Location Techniques – Infrared Intercept Systems – Communications ESM & Communication Intelligence.	10
IV	Electronic countermeasure systems	Introduction – Off Board Passive & Active ECM Systems – On Board Passive & Active ECM Systems – ECM Techniques (Any3) – Infrared Countermeasures – Communications Countermeasures – Information Warfare.	10
V	Electronic counter- countermeasure systems	Introduction – Tracking RadarCounter- Countermeasures – Infrared Counter- Countermeasures – Communications Counter – Countermeasures – New Electronic Defence Architectures.	10

Text Book :

1. IntroductionToElectronicDefenseSystems-FilippoNeri-NewAgeInternational-SecondEdition- Rs.395/-

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B.SC. ELECTRONICS AND COMMUNICATION SEMESTER VI ELECTIVE III - PAPER IV - GROUP C NANOELECTRONICS

COURSE OBJECTIVES: Students will try to learn:

To introduce the nanoelectronics, nanodevices, spintronics and molecular electronics. Understand the electronic device fabrication describe the principle and the operation of Nano electronic devices. In-depth technical knowledge in one or more areas of specialization.

COURSE OUTCOMES

CO1	Ability to perform simple analysis of Nano electronic devices and calculate the density of states in Nano electronic devices.
CO2	Ability to perform in-depth analysis of self-assembly in Nano electronic devices
CO3	Nano Electronics and Nano Micro fabrication course is designed to encompass all these aspects, viz., nano and micro regime design, simulation and fabrication and all types of IC's, micro fluidics.
CO4	It is expected that, after undergoing this course, the students will acquire both theoretical knowledge and practical skills in diverse upcoming areas of current technology.

<u>Syllabus</u>				
Unit	Unit Title	Intended learning Chapters	Hours of	
		(Programme specific qualification Instr		
		attributes K1, K2, K3, K4)		
	I Introduction &	What is Nanotechnology - Advantages -		
Ι		Scope - Limits of Nanotechnology -	10	
	ethical issues	Solutions cause Problems - Change causes		

10	
10	
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10	
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REFERENCE BOOKS:

- 1. NanoTechnology-AFutureTechnologyWithVisions-AppinLABS-BPB-Rs.270/-
- 2. Nano:TheEssentials-UnderstandingNanoScience&NanoTechnology--TPradeep-TMH

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B.SC. ELECTRONICS AND COMMUNICATION

Professional Competency Skill COMPETITIVE SKILLS

50 Multiple Choice Questions. EACH QUESTION 1.5 MARKS.

TEN Multiple Choice Questions from Each Unit

COURSE OBJECTIVES:

To provide students with solid foundation in CSE so that they are able to use this knowledge in getting jobs and maintaining their jobs. To develop students with professional and ethical attitude, effective communication skills and the attitude of working in group/with people for successful careers.

COURSE OUTCOMES: After completion of this course, student able to

CO1	The ability to analyze a problem and to identify the appropriate Verbal reasoning.
CO2	The ability to apply nonverbal reasoning.
CO3	An understanding of professional, ethical and social responsibilities.
CO4	The ability to communicate effectively with a range of audiences.
CO5	The ability to succeed in competitive exams

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Verbal reasoning	Analogy – Classification – Direction Sense Test – Logical Sequence of Words – Inserting The Missing Character – Situation Reaction Test.	05

PE	PERIYAR UNIVERSITY				
II	Nonverbal reasoning	AnalyticalReasoning–MirrorImages– WaterImages-CompletionofIncompletePattern– CubesandDice.	05		
III	Arithmetical, ability	Average–ProblemsonAge–Percentage– Profit&Loss–Ratio∷–ChainRule– Time&Work.	05		
IV	Logarithms	Pipes&Cistern–Time&Distance– ProblemsonTrains-Boats&Streams– SimpleInterest–Compound Interest–Logarithms.	05		
v	Charts	Area–Calendar–Clocks–Heights & Distances– BarGraphs–PieCharts.	05		

ReferenceBooks

1. A Modern Approach to Verbal & Non Verbal Reasoning - Revised Edition – R.S. Aggarwal – S. Chand.(Units:I&II)–Rs.750/-

- 2 QuantitativeAptitude-RevisedEdition-R.S.Aggarwal-S.Chand.Units:III,IV&V)-Rs.440/-
- 3. AnAdvancedApproachtoDataInterpretation-R.S.Aggarwal–S.Chand.
- 4. AdvancedObjectiveGeneralKnowledge-R.S.Aggarwal–S.Chand
- 5. ObjectiveGeneralEnglish-R.S.Aggarwal-S.Chand

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V & VI

CORE PRACTICAL IV

IC'S & COMMUNICATION LAB

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 22 Experiments)

COURSE OBJECTIVES:

The main aim of this lab is to teach the linear and non-linear applications of operational amplifiers (741). Students are made familiar with theory and applications of 555 timers. Students are made to Design combinational logic circuits using digital ICs&To acquire the basic knowledge of special function ICs

COURSE OUTCOMES:

CO1	Design and analyses the various digital circuits.
CO2	Design and analyses the various linear & nonlinear application of op-amp.
CO3	Design and analyses oscillators and multivibrator circuits using op-amp& Timers.
CO4	Design and analyses the various communication application of op-amp.
CO5	Practice the basic mechanics of conversions.

<u>Syllabus</u>

- 1. NAND GATE Implementation Using TTL.
- 2. NOT/NAND/NOR GATE Implementation Using IIL/CMOS.
- 3. OR/NOR GATE Implementation Using ECL.
- 4. Inverting/Non-Inverting Amplifier using Op-Amp
- 5. Inverting/Non-Inverting Adder Using Op-Amp.

- 6. Subtractor Using Op-Amp.
- 7. Multiplier/Divider Using Op-Amp.
- 8. Differentiator/Integrator Using Op-Amp.
- 9. Low Pass/High Pass Filter Using Op-Amp.
- 10. Band Pass/Band Reject Filter Using Op-Amp.
- 11. Voltage to Current Converter (Grounded load).
- 12. Current to Voltage Converter.
- 13. Solving of Simultaneous Equations Using Op-Amp.
- 14. Square Wave Generation Using Op-Amp
- 15. Triangular Wave Generation Using Op-Amp
- 16. Phase Shift Oscillator Using Op-Amp.
- 17. Wein Bridge Oscillator Using Op-Amp.
- 18. Amplitude Modulation & Demodulation.
- 19. Pulse Amplitude Modulation & Demodulation.
- 20. Pulse Width Modulation & Demodulation.
- 21. Astable Multivibrator Using 555IC
- 22. Monostable Multivibrator Using 555 IC Triangular
- 23. Waveform Generation Using 555 IC
- 24. Voltage Controlled Oscillator Using 555IC
- 25. Schmitt Trigger Using 555IC
- 26. Frequency Multiplication Using 565 IC
- 27. Binary Weighted Resistor Type of DAC
- 28. R-2R Ladder Type of DAC.
- 29. ADC Using ADC 0809
- 30. DAC Using DAC 0804

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B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V & VI

CORE PRACTICAL V- Group D

8051 MICROCONTROLLER & INTERFACING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

Course Objectives

To expose students to the operation of typical Microcontroller (8051)**simulation tool**/ trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Identify relevant information to supplement to the Microcontroller (8051).
CO2	Set up programming strategies and select proper mnemonics and run their program on
	the training boards.
CO3	Practice different types of programming keeping in mind technical issues and
	evaluate possible causes of discrepancy 2experimental observations in comparison.
CO4	Develop testing and experimental procedures on Microcontroller (8051)analyze their
	operation under different cases.
CO5	Prepare professional quality textual and computational results, incorporating
	accepted data analysis and synthesis methods, simulation software, and
	word-processing tools.

Syllabus

- 1. Addition of Two 8-Bit Numbers & Subtraction of Two 8-Bit Numbers
- 2. Multiplication of Two 8-Bit Numbers
- 3. Division of Two 8-Bit Numbers

- 4. BCD Addition
- 5. BCD Subtraction
- 6. FILL
- 7. BLOCK MOVE
- 8. 1's & 2's Complement of a 16-Bit Number.
- 9. Smallest/Largest of N Numbers.
- 10. Ascending/Descending Order of N Numbers.
- 11. Sum of N 8-Bit Numbers.
- 12. Multi-Byte Addition.
- 13. 3-Digit Decimal to Two-digit Hexadecimal Conversion.
- 14. Two-digit Hexadecimal to 3-digit Decimal Conversion.
- 15. ADC Interface.
- 16. DAC Interface.
- 17. Waveform Generation Using DAC.
- 18. Stepper Motor Interface.
- 19. Interface With Switches & Interface with LED's
- 20. Hex Keyboard Interface.
- 21. Interface with Single Seven Segment Display.
- 22. Interfacing With Multiple Seven Segment Displays.

- 23. Interface with LCD
- 24. Interfacing with Solid State Relay.
- 25. Moving Display message with LCD
- 26. Blinking Display message with LCD
- 27. Digital Clock Using RTC.
- 28. Traffic Light Control Interface.

L	Т	Р	С
2	1	2	3

B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V & VI

CORE PRACTICAL V- Group D

16F877A MICROCONTROLLER & INTERFACING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

Course Objectives

To expose students to the operation of typical Microcontroller (16F877A) simulation tool/ trainer kit. solve different problems by developing different programs & to develop the quality of assessing and analyzing the obtained data.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Identify relevant information to supplement to the Microcontroller (16F877A).		
CO2	Set up programming strategies and select proper mnemonics and run their program		
	on the training boards.		
CO3	Practice different types of programming keeping in mind technical issues and		
	evaluate possible causes of discrepancy in practical experimental observations in		
	comparison.		
CO4	Develop testing and experimental procedures on Microcontroller (16F877A)analyze		
	their operation under different cases.		
CO5	Prepare professional quality textual and computational results, incorporating		
	accepted data analysis and synthesis methods, simulation software, and		
	word-processing tools.		

Syllabus

- 1. Additionof Two8BitNumbers & SubtractionofTwo8BitNumbers
- 2. MultiplicationofTwo8BitNumbers

- 3. Division of Two 8-Bit Numbers
- 4. BCD Addition
- 5. BCD Subtraction
- 6. FILL
- 7. BLOCK MOVE
- 8. 1's&2's Complement of a 16-Bit Number.
- 9. Smallest/Largest f N Numbers.
- 10. Ascending/Descending Order of N Numbers.
- 11. Sum of N 8-Bit Numbers.
- 12. Multi-Byte Addition.
- 13. 3-Digit Decimal to Two-digit Hexadecimal Conversion.
- 14. Two-digit Hexadecimal to 3-digit Decimal Conversion.
- 15. ADC Interface.
- 16. DAC Interface.
- 17. Waveform Generation Using DAC.
- 18. Stepper Motor Interface.
- 19. Interface With Switches & Interface with LED's
- 20. Hex Keyboard Interface.
- 21. Interface with Single Seven Segment Display.
- 22. Interfacing With Multiple Seven Segment Displays.

- 23. Interface with LCD.
- 24. Interfacing With Solid State Relay.
- 25. Moving Display message with LCD
- 26. Blinking Display message with LCD
- 27. Digital Clock Using RTC.
- 28. Traffic Light Control Interface.

L	Т	Р	С
2	1	2	4

B.SC. ELECTRONICS AND COMMUNICATION

SEMESTER V & VI

CORE PRACTICAL V

PLC PROGRAMMING LAB

(ANY EMBEDDED BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 17 Experiments)

Course Objectives

To explain the basic concepts of a Programmable Logic Controller. Students will be able to state basic PLC terminology and their meanings. Students will be able to explain and apply the concept of electrical ladder logic, its history, and its relationship to programmed PLC instruction.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Students will be able to describe typical components of a Programmable Logic
	Controller.
CO2	Students will be able to explain and apply the concept of electrical ladder logic,
	its history, and its relationship to programmed PLC instruction.
CO3	Students will be able to explain the concept of basic digital electronics and data
	manipulation.
CO4	Students will be able to use timer, counter, and other intermediate programming
	functions
CO5	Students will be able to use timer, counter, and other intermediate programming
	functions

- 1. Study of PLC Symbols
- 2. Study of Various Logic Executions in Ladder Diagram.

- 3. Writing of Ladder Logic for Different Statements.
- 4. Ladder Diagram Development for Different Types of Logic Gates using Suitable Software
- 5. PLC Input-Output Wiring Methods
- 6. Operating Simple Loads using Relays ,Switches and Push buttons
- 7. Different Applications of Push Buttons
- 8. Programming the PLC Via Ladder logic
- 9. Working of Different Types of Timers
- 10. Study and Implement ON delay timer in PLC
- 11. Study & implement OFF delay timer in PLC
- 12. Working Of Different Types of Counters
- 13. Study& Implementation of up counter in PLC Programming.
- 14. Study& Implementation of Down Counter in PLC Programming.
- 15. Interlocking
- 16. Sequencer
- 17. Sequential Operation of On/Off of A Set of Lights
- 18. Forward and Reverse Direction Control of Motors
- 19. Latching and Unlatching of motor
- 20. Prepare the Physical and Programmed Ladder Diagram for the Control Problem shown below & Implement the same.
- 21. PLC Programming for Bottle Filling Plant.
- 22. Procedure for Producing a Ladder Logic Diagram for Car Parking Simulation
- 23. Position Control for Satellite Dish DC Motors
- 24. Starting Three Phase Induction Motors Via Star- Delta Starter
- 25. Automatic Indication of Water Tank Level
- 26. Traffic Lights Indication

B.SC. ELECTRONICS AND COMMUNICATION

CIRCUIT IMPLEMENTATION USING ARDUINO UNO BOARDS COMMON FOR ALL PRACTICALS UNDER GROUP D

L	Τ	P	С
2	1	2	4

ANY FIVE

Course Objectives

To provide knowledge of different Smart System applications. To familiarize students with Arduino as IDE, programming language & platform. To provide knowledge of Arduino boards and basic components. Develop skills to design and implement various smart system application.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Familiar with Arduino environment and its applications.
CO2	Able to understand Arduino programming with C++.
CO3	Able to Design Smart systems applications.
CO4	Learn and understand about any new IDE, compiler, and MCU chip in Arduino

- 1. LED Control Through Push Buttons
- 2. Photo Resistor as Light Intensity Detector
- 3. DC Motor Direction Control using L293D
- 4. DC Motor Speed Control using L293D
- 5. Displaying Texton LCD Display
- 6. Scrolling Texton LCD Display
- 7. Interfacing 4x3 Keypad and LCD
- 8. Displaying Alphanumeric Characters on Dot Matrix Display

- 9. Display of Numeric Characters using 7-Segment Display.
- 10. Unipolar Stepper Motor Speed Control Using ULN2003A
- 11. LED Control Through Serial Communication
- 12. LED Control using Infrared
- 13. Displaying Current Date and Time on 16x2 LCD
- 14. Switching Control of AC Load using TRIAC
- 15. Voltage Regulation Across AC Load using TRIAC
- 16. Switching Control of AC Load Through SMS Text Message

REFERENCEBOOK

Arduino Projects for Engineers - Neerparaj Rai - BPB Publications - Rs. 297/-

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4	0	0	4

B.SC. ELECTRONICS AND COMMUNICATION

APPLIED ELECTRONICS - I (ALLIED)

(For B.Sc CS, BCA, B.Sc IT, &B.Sc PHY)

I/II YEAR-I/III SEMESTER

COURSE OBJECTIVES:

- To study about the fundamentals of semiconductor.
- To gain the basic knowledge of electronic components and the function of power supply.
- To understand the operation of oscillator, amplifier and filters.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the principle of semiconductor devices.		
CO2	Have a thorough understanding of the fundamental concepts of electronic		
	components		
CO3	Analyse and design various the conceptual theory of the power supply.		
CO4	Distinguish between the sinusoidal and non-sinusoidal waveforms.		
CO5	Acquire the knowledge on the types and principles of oscillators, amplifiers and		
	filters.		

<u>Syllabus</u>

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
Ι	Semiconduct or Theory	Intrinsic Semiconductor – Extrinsic Semiconductor – Theory of PN Junction diode – Zener Diode – Avalanche Breakdown – Zener Break down – Operation of PNP &NPN Transistor – CB , CE , CC Configuration and Characteristics – Transistor as an Amplifier.	10

II	Resistors,	Resistors, Capacitors & Inductors in Series and Parallel	10
	Capacitors	- Factors governing the Resistance of a Resistor,	
	& Inductors	Capacitor & Inductor - Colour Coding of Resistors -	
		Various Other Passive & Active Devices - Ohm's	
		&Kirchhoff Laws - Series Circuits - Parallel Circuits -	
		Series & Parallel Circuits - Open Circuit - Short Circuit	
III	Power	Half Wave Rectifier - Full Wave Rectifier - Bridge	10
	Supply's	Rectifier - Capacitor Filter - Fixed IC Regulated Power	
		Supply using 78XX - Dual IC Regulated Power Supply	
		using 78XX & 79XX .	
IV	Waveforms	Sinusoidal Waveform - Non-Sinusoidal Waveform -	10
		Peak Value - Peak to Peak Value - Average Value -	
		RMS Value – Period & Frequency Measurement – Use	
		of Digital Multimeter – Use of CRO.	
V	Oscillators,	Barkhausen Criterion – SinusoidalOscillators:Hartley,	10
	Amplifiers &	Phase Shift & Crystal Oscillator. Non Sinusoidal	
	Filters	Oscillators: Astable, Monostable & UJT as a Sawtooth	
		Oscillator. Amplifiers: RC Coupled & Transformer	
		Coupled Amplifiers. Filters : Low Pass, High Pass,	
		Band Pass & Band Reject Filters .	

Books for Study

- 1. Electronic Devices & Circuits S. Salivahanan TMH II Edition
- 2. Circuits & Networks Sudhakar TMH 4th Edition.

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4			4

B.SC. ELECTRONICS AND COMMUNICATION

APPLIED ELECTRONICS - II (ALLIED)

(For B.Sc CS, BCA, B.Sc IT, &B.Sc PHY)

I/II YEAR-II/IV SEMESTER

COURSE OBJECTIVES:

- To study about the fundamental's concepts of transducers and op-amps.
- To gain the basic knowledge of IC and PCB fabrication process.
- To understand the fundamentals of communication principle, modulation techniques, transmission and reception.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Understand the principle and types of the various transducers.	
CO2	Analyses and deign basic op-amp circuits, non-linear circuits, comparator, signal	
	generators, and oscillator.	
CO3	acquire qualitative knowledge about the fabrication process of integrated	
	circuit& PCB fabrication	
CO4	Comprehend the working principle of the communication systems.	

		Intended learning Chapters	Hours of
Unit	Unit Title	(Programme specific qualification attributes K1,	Instructio
		K2, K3, K4)	n
		Resistive Transducers – Inductive Transducers -	
	Transducers	Capacitive Transducers - Piezo Electric Transducer –	
Ι	Transuucers	Thermo-Electric Transducers – Temperature	10
		Transducers–Instrumentation Amplifier –	
		Microphones & Loud Speakers.	
		Introduction - The ideal OP-AMP - OP-Amp stages -	
	Op – Amp's	OP-Amp parameters - Inverting & Non Inverting	
II		Amplifier - Adder - Subtractor - Multiplier - Divider -	10
		Integrator - Differentiator - V to I Converter - I to V	
		Converter.	
	IC	Basic Planar Process - Fabrication of a Typical Circuit	
III	Fabrication	- Active and Passive Components - Fabrication of	10
	Process	FET, MOSFET & CMOS - Thin & Thick Film	

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		Technology.	
IV	PCB Fabrication Process	PCB Types – Layout & General Rules – Design Rules For Digital Circuit PCB's – Artwork – Properties & Types of Copper Clad Laminates – Photo Printing – Screen Printing – Types of Etchants – Manual Routing – Auto Routing – Design Rule Check.	10
v	Need For Modulation – Amplitude Modulation &CommunicatiDetection - Frequency Modulation & Detection – AM		10

Books for Study

- 1. Electronic Devices & Circuits S. Salivahanan TMH II Edition
- 2. PCB Design Walter.C. Bosshart TMH
- 3. Electronic Communication Systems Kennedy TMH IVEdition.

B.SC. ELECTRONICS AND COMMUNICATION

APPLIED ELECTRONICS LAB (ALLIED) (For B.Sc CS, BCA, B.Sc IT, &B.Sc PHY)

I/II YEAR-II/IV SEMESTER

(ANY HARDWARE BASED SIMULATION TOOL MAY ALSO BE USED)

(Any 18 Experiments)

Course Objectives

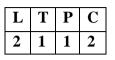
PERIYAR UNIVERSITY

To introduce electric circuits and its analysis &to impart knowledge on solving circuits using network theorems, digital circuits & electronic circuits.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Analyze the characteristics of PN Junction diode, transistors
CO2	To impart knowledge on solving circuits using network theorems
CO3	Determine frequency response of RC circuits and simulate series, parallel resonant circuits.
CO4	Learn principal of digital Electronics .
CO5	Demonstrate the applications of op-amp & timer

- 1. PN Junction Diode Characteristics
- 2. CE Input Characteristics
- 3. CE Output Characteristics
- 4. Ohm's Law
- 5. Kirchhoff^{*}s Current Law.
- 6. Kirchhoff^{*}s Voltage Law.
- 7. Resistors in Series & Parallel
- 8. Capacitors in Series & Parallel
- 9. Measurement of Amplitude & Frequency Using CRO.



- 10. NAND as a UNIVERSAL GATE(AND/OR)
- 11. NOR as a UNIVERSAL GATE(AND/OR)
- 12. Verification of De-Morgan's Theorem.
- 13. Truth Table Verification of BASIC Gates (Any one gate)
- 14. Half -Adder/Full -Adder
- 15. Half- Subtractor/Full- Subtractor.
- 16. Encoder Using 74147 IC
- 17. Decoder Using 7442 IC
- 18. Multiplexer Using 74153 IC
- 19. Demultiplexer Using 74155 IC
- 20. Clock Generation Using NAND/NOR GATE
- 21. Full Wave Rectifier with Capacitor Filter.
- 22. Dual IC Regulated Power Supply(78XX&79XX).
- 23. Inverting Adder/Non-Inverting Adder Using-Amp
- 24. Subtractor Using-Amp
- 25. Low pass Filter/ High Pass Filter.

PERIYAR UNIVERSITY B.SC. ELECTRONICS AND COMMUNICATION

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ELECTRONICS – I (ALLIED) For B.Sc [MATHS,STATISTICS,MATHS (CA) , BIO CHEMISTRY, MICROBIOLOGY, BIO-TECH]

I/II YEAR-I/III SEMESTER

COURSE OBJECTIVES:

To understand operation of semiconductor devices. DC analysis and AC models of semiconductor devices. To verify the theoretical concepts through laboratory and simulation experiments.

CO1	Understand the current voltage characteristics of semiconductor devices,
CO2	Analyze dc circuits and relate ac models of semiconductor devices with their physical Operation,
CO3	Design and analyze of electronic circuits Laws
CO4	Evaluate frequency response to understand behavior of waveforms.

Unit	Unit Title	Intended learning Chapters	Hours of
		(Programme specific qualification	Instruction
		attributes K1, K2, K3, K4)	
		Intrinsic Semiconductor – Extrinsic	10
		Semiconductor - Theory of PN	
		Junction Diode - Zener Diode -	
т	Semiconductor	Avalanche Breakdown - Zener Break	
L	theory	Down - Operation of PNP & NPN	
		Transistor - CB,CE, CC Configuration	
		and Characteristics - Transistor as an	
		Amplifier.	
		Resistors, Capacitors & Inductors in	10
	Resistors, capacitors	Series and Parallel - Factors	
II		GoverningResistance, Capacitance	
	& inductors	&Inductance - Colour Coding of	
		Resistors - Energy Stored in a	

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		Capacitor - Energy Stored in an	
		Inductor - Various Other Passive &	
		Active Devices.	
		Ohm's Law - Kirchhoff's Current Law	10
	Circuit laws	- Kirchhoff's Voltage Law - Voltage	
TTT		Division - Current Division - Series	
III		Circuits - Parallel Circuits - Series &	
		Parallel Circuits - Open Circuit - Short	
		Circuit.	
	Waveforms	Sinusoidal Waveform - Non-Sinusoidal	10
		Waveforms - Peak Value - Peak to	
IV		Peak Value - Average Value - RMS	
		Value – Period & Frequency	
		Measurement	
		Half Wave Rectifier - Full Wave	10
	Power supply	Rectifier - Bridge Rectifier - Capacitor	
• 7		Filter - Fixed IC Regulated Power	
V		Supply using 78XX - Dual IC	
		Regulated Power Supply using 78XX	
		& 79XX.	

REFERENCE BOOKS:

- 1. Electronic Devices & Circuits S. Salivahanan TMH II Edition
- 2. Circuits & Networks Sudhakar TMH 4thEdition.

B.SC. ELECTRONICS AND COMMUNICATION

ELECTRONICS – II (ALLIED)

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For B.Sc. [MATHS, STATISTICS, MATHS (CA), BIO CHEMISTRY,MICROBIOLOGY,BIO-TECH] I/II YEAR-II/IV SEMESTER

COURSE OBJECTIVES:

- To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronic circuits.
- To prepare students to perform the analysis and design of various digital electronic circuits.
- To learn the design process of registers, counters and conversion of analog to digital conversion and vice-versa.

COURSE OUTCOMES: At the end of course, student will be able to:

CO1	Examine the structure of various number systems and its application in digital design.
CO2	Have a thorough understanding of the fundamental concepts and minimization techniques used in digital electronics.
CO3	Understand various Boolean algebra and its applications
CO4	Understand various combinational logic circuits and its applications
CO5	Analyze and design various Electronic circuits and its applications

Unit	Unit Title	Intended learning Chapters (Programme specific qualification attributes K1, K2, K3, K4)	Hours of Instruction
I	Number systems	Introduction - Binary Number System - Octal Number System - Decimal Number System - Hexadecimal Nmber System - Conversion From One System to Another.	10
II	Binary rules	Binary Addition - Binary Subtraction -	10

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	Binary Multiplication - Binary Division					
		- 1's & 2's Complement Subtraction -9's & 10's Complement Subtraction.				
		Logic GATES - NAND as a	10			
III	Boolean algebra	UNIVERSAL GATE - NOR as a				
		UNIVERSAL GATE - Basis Laws of				
		Boolean Algebra - Principle of Duality				
		– De Morgan's Theorem.				
		Half Adder - Full Adder - Half	10			
	Combinational	Subtractor - Full Subtractor - Encoder -				
IV	elements	Decoder - Multiplexer –				
		Demultiplexer.				
		Barkhausen Criterion – Sinusoidal	10			
		Oscillators (Hartley, Phase Shift				
		&Crystal Oscillator) - Amplitude				
		&Frequency: Modulation & Detection				
		– Amplifiers: RC Coupled &				
\mathbf{V}	Electronic circuits	Transformer Coupled. Filters : Low				
		Pass, High Pass, Band Pass & Band				
		Reject Filters – Op-Amp's & Their				
		Applications (Addition , Subtraction,				
		Multiplication, Division,				
		Differentiation and Integration).				

REFERENCE BOOKS:

- 1. Digital Circuits & Design– Salivahanan-Vikas Pub III Edition.
- 2. Electronic Devices & Circuits S. Salivahanan TMH II Edition

B.SC. ELECTRONICS AND COMMUNICATION ELECTRONICS LAB (ALLIED)



For B.Sc. [MATHS, STATISTICS, MATHS (CA), BIOCHEMISTRY, MICROBIOLOGY, BIO-TECH] I/II YEAR-II/IV SEMESTER

(Any 18 Experiments)

Course Objectives

To introduce electric circuits and its analysis &to impart knowledge on solving circuits using network theorems, digital circuits & electronic circuits.

COURSE OUTCOMES: After completion of this course, student able to

CO1	Analyze the characteristics of PN Junction diode, transistors
CO2	To impart knowledge on solving circuits using network theorems
CO3	Determine frequency response of RC circuits and simulate series, parallel resonant circuits.
CO4	Learn principle of digital Electronics.
CO5	Demonstrate the applications of op-amp & timer

- 1. PN Junction Diode Characteristics
- 2. CE Input Characteristics
- 3. CE Output Characteristics
- 4. Ohm's Law
- 5. Kirchhoff's Current Law.
- 6. Kirchhoff^{*}s Voltage Law.
- 7. Resistors in Series & Parallel

- 8. Capacitors in Series & Parallel
- 9. Measurement of Amplitude & Frequency Using CRO.
- 10. NAND as a UNIVERSAL GATE (AND/OR)
- 11. NOR as a UNIVERSAL GATE (AND/OR)
- 12. Verification of De-Morgan's Theorem.
- 13. Truth Table Verification of BASIC Gates (Any one gate)
- 14. Half Adder/Full Adder
- 15. Half Subtractor/Full Subtractor.
- 16. Encoder Using 74147 IC
- 17. Decoder Using 7442 IC
- 18. Multiplexer Using 74153 IC
- 19. Demultiplexer Using 74155 IC
- 20. Clock Generation Using NAND/NOR GATE
- 21. Full Wave Rectifier with Capacitor Filter.
- 22. Dual IC Regulated Power Supply (78XX&79XX).
- 23. Inverting Adder/Non-Inverting Adder Using-Amp
- 24. Subtractor Using-Amp
- 25. Low pass Filter/High Pass Filter.

Minutes of BOS In UG Elec & Comm :

Approval of the Syllabus for BSc Electronics and Communication under CBCS to be implemented Academic year 2023-2024.

MEMBERS of the BOARD

Sl.No	Name and Address	Designation of the Board	Comments	Sign
1.	Mrs.S.Vennila Assistant Professor, Dept of Electronics and Communication, Govt. Arts College, Dharmapuri. Ph:9488873540	Chairman	I have approved the content of the syllabus	Baringoon
2.	Mrs.M.Mahalakshmi Assistant Professor, Department of Electronics and Communication Selvam Arts &Science Colllege Namakkal — 637003. Ph:95979409 10	Member	I HAVE ACCEPTED	M.Yur.
3.	Dr. L.Arunraja Assistant Professor, Department of Electronics and Communication K.S.R. College of Arts &Science Tiruchengode — 637215.Ph:9047209897	Member	I HAVE ACCEPTED	L. Arming
4.	 Mr. S. Santhosh Assistant Professor, Department of Electronics and Communication Muthayammal College of Arts & Science Rasipuram — 637408. Ph: 8056213809 	Member	I HAVE ACCEPTED	Lutham.
5.	Mr.T. Suresh Assistant Professor, Department of Electronics and Communication Salem Sowdeswari College, Salem - 636010. Ph:92455 1902 1	Member	I HAVE ACCEPTED	J. Josh
6.	Dr.P.Maadeswarari Assistant Professor Department of Energy Science and Technology, Periyar University, Salem — 636011, Ph:9443896613	University Nominee	I HAVE ACCEPTED	P. Maglernionen
7.	Dr.O.M.Saravanakumar Associate Professor & Head, Department of Electronics and Communication Systems Sri Krishna Arts and Science College Sugunapuram, Kuniamuthur, Coimbatore- 641008, Ph:9488350744 E.Mail:saravanakumarom@akasc.ac.in	External Member	I have accepted the content of the syllabus.	Offer tim
8.	K. Rajendran Assistant Professor, Department of Electronics Lrg Government Arts College For Women, Tirupur 641 604. Mobile No. 8248'784986 E-Mail:krishrazen gmai1.com	External Member		
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PER	<u>IIAK UNIVERSIII</u>			
10.	Mr. A. Gangatharan PCB Design Engineer INSEMI Technologies PVT LTD Bangalore, Karnataka 560066	Alumni		
	Phone No :6382332750 Email ID : raguganga7 gmail.com			